

Promoted Architecture for the Multi tab 2-D HAAR Wavelet Filter banks

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ABSTRACT:

Image processing is one of major application in VLSI systems, more over the performance also the important consideration along with the major VLSI consideration such as area, speed and power. In this we proposes the advanced wavelet transformation method which is of parallel processing .In general transformation mechanisms design can be done using the execs utilization of multipliers and flip-flop which causes the increases in area and power and also frequency issues.

That can overcome by the shift-add multiplier less algebraic integer poly-encoding of haar wavelet filters with the error free computation. The proposed design not only consists of the basic advantages and also the good noise rejection construction. SNR and PSNR comparisons can be carried out using haar-4 and -6 designs for different word lengths. Approximately 38% Improvement SNR and PSNR improvements were observed with our proposed method. Haar-4 and -6 designs are ported into Spartan-6 by using the Xilinx 14.3 and also verified using the chip scope pro and EDK, SDK.

I.INTRODUCTION:

The wavelet transform is rapidly used for image coding. Because in it has the progressive image transmission concept features like quality or resolution scaling. Wavelets are crucial component of different systems like JPEG2000 system, MPEG-4. Discrete wavelet transforms (DWT) can draw favorable attention due the main features of disintegrating the image or signal onto explode of basic functions that are with the apparitional properties. Many signal processing systems relays on wavelet filter banks [10]. Wavelets are employed in numerical analysis also Wavelets can be utilized [11], [12], real-time commutations [11], image enhancement and compression [3], [13], pattern

recognition [11], biomedical [12], approximation Theory, computer graphics and image, standard video coding like (H.265). Specified classes of discrete wavelet transform are Daubechies wavelets and Haar wavelets. These are very much used and well adopted in image processing applications [3].

In this concern Haar wavelets are generated from 4- and 6-tap filter banks as Daub-4 and -6 wavelets these are attractive in systems like where the applied signals can varies slowly and smother in nature. The haar-6 wavelets can be used in such applications whereas the signals behavior has nature of precipitous changes, transfixes, and grater unexpected noise levels where as harr-4 can be at the smoother side. These have diligences like medical imaging advanced endoscopy techniques in which image detailed and specified particular information analysis can be pertained [11]. These DWT are can be ascertained to the set of sub functions that can be fulfilled by the chain of sub filters. These can fragmented the signal into the set of sub bands. In specific, multi-resolution analysis of 2-D Can be obtained by sub-band coding.

In this paper, we nominate a novel multi-encoding technique that accomplishes the exact computation of multi-level 2-D Haar wavelet transforms using algebraic integer (AI) encoding. And can be analyzed with existing AI designs in literature [1]–[3], [6], [11], [13], the proposed design can work out approximations of wavelet image throughout integer fields and with one final reconstruction step exclusively with AI based 2-D architecture. The design can avoid the unnecessary intermediate reconstruction steps. Furthermore, in our method of design we are not utilizing any multiplier. As the multiplier is more power consuming element in digital circuit designing that makes our design good accuracy, speed and also less foot print that may effects the design cost also.

The proposed novel design is poly encoded and poly-rate; can operating on no intermediate reconstruction steps using AI. In this model, error-less figuring can be performed until the final FRS. Our architecture underlines the major issues such as image quality, frequency and power consumption. The rest of this brief is organized as follows. Section II delineates the literature survey of AI. Section III carries out the principles of sub-band coding by means of haar-4 and -6 filters. Section IV renders the mathematical approach pattern of AI encoding into the 2-D sub-band coding context. Wavelet sub-band coding using poly-encoding with AI bases are provided for poly-level decay, considering both haar-4 and -6 filter banks.

II. LITERATURE SURVEY:

A. Fixed-Point Errors effect:

Filter banks associated to Haar wavelets have coefficients of real but not expressible as the quotient of two integers. In order to represent in fixed fixed-point representation that requires rounding off or truncation [2], [6]. So these predicted approximations can propagate the errors in the certain filter banks and moreover the filter banks of longer chain can cause the huge considerable error rate. This results on the signal-to-noise consideration.

B. Prior Art on AI-Based DWT:

AI encoding can be the promising concern to the computation noise which can be occurred in wavelet design systems [6]. Opened up by Cozzens and Finkelstein [3] AI quantization has been adopted in different systems like signal processing, wavelet and discrete cosine transform analysis [1], [11], [13]. A substantial advantage of AI encoding has the mapping potentiality of converting the irrational wavelet coefficients into vectors or arrays of integers. Hence the decomposition of wavelet can be carried out with error free operation which consists the purely integer operations of a vector represented framework. Thus, the operational coefficients of the haar filters can be represented into integers, accordingly based on the selected AI basis [3], [6]. Reconstructing setup are needed in AI encoding schemes to convert arithmetic quantities into the binary fixed format. 1-D haar -4 and -6 filters architectures were opened up by Wahid and Dimitrov in the recent past.

Significantly, the 2-D architectures illustrated by Wahid et al. [1]–[3], [6], [13] which has the element requirement of intermediate operations i.e. intermediate reconstruction steps that has the feature of function that can convert the AI encoded transform coefficients into the fixed-point format. When decomposition of poly-level designs can associate the problem of combined structure of intermediate reconstruction stages at individual stage of filter [3], [6], [13]. Error in occurrence in the intermediate reconstruction stage extenuate the features 2-D multi-level DWTs using AI encoding. This can be the considerable issue that identified at the considerable literature survey.

C. Proposed Encoding Scheme:

The above stated problem can be compensated by proposing the poly-encoding which can exhibit error-free computation concern with the 2-D decomposition levels. In this proposed method the final level of decomposition and filtering only consists the reconstruction step hence intermediate multiplier reconstruction steps can be eliminated. Unlike the previous described schemes [2], [3], [6] the operation of our proposed scheme works with the AI representation only until the final reconstruction block—minus of intermediate reconstruction steps.

Hence, the occurrence of error possibility is only at the FRS. We propose a novel architecture based on AI for sub-band coding of images using 2-D Haar-4 and -6 wavelet filters. The approaches of AI quantization can efforts to an architecture owning a parallel channel structure. Several levels of decomposition of input data can be done based on the application demand. Canonical signed digit (CSD) constant coefficient multipliers are used for the FRS. The stated method extending features like less architecture complexity etc. This architecture incorporates less level of coupled and uncorrelated quantization noise in resultant decomposed image data at the final level.

III. CRITIQUE OF SUB-BAND CODING:

Image data can decompose into basic wavelets can be done by sub band coding. An approximation and detail sub-images of input data can be done by a 2-D finite impulse response (FIR) filter bank processes.

The algorithmic operation can be performed; the signal flow architecture can have re-submitted resulting approximation as shown in fig 1. That causes commoner approximation after the every iteration. Hence that can be analyzed by the recursive diagram of poly-level wavelet analysis as shown in Fig 2. A commoner approximation can be furnished after each filter bank set. Detail information can be provided by the each individual level.

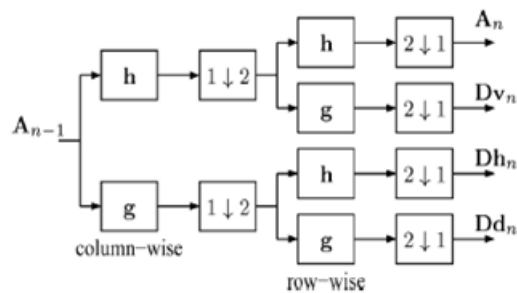


Fig.1. Diagram of a single application of the 2-D wavelet filter bank.

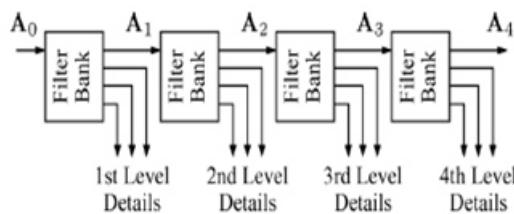


Fig.2. Recursive application of the 2-D wavelet filter bank.

In this work, we concentrated on the computation of the commoner approximations A_n , The signal flow of topmost branch in Fig. 1 estimates to approximation data. The 2-D FIR filter bank designed based on the Haar-4 and -6 filter bank [2]. A low-pass filter which associate with these filter banks be denoted respectively.
h (Haar-4) and h (Haar-4)
These particular filters possess irrational quantities as shown below [2], [3], [6]:

IV.AI-BASED HAAR-4 AND -6 SCALING FILTERS:

Therefore, the Haar-4 and -6 filter bank psychoanalysis can be splitted into two/three structures.

This eases integer channel structure of two/four, where the integer coefficient filters are considered This has the major parts like split, Predictor and Updater. The basic operation of splitting is can be that separates the total samples into the set of basic two functions Haar expansion is a two-point average and difference operation. The basic functions are given as

$$\varphi_{2k}[n] = \begin{cases} 1/\sqrt{2}, & n=2k, 2k+1 \\ 0, & \text{otherwise} \end{cases}$$

$$\varphi_{2k+1}[n] = \begin{cases} 1/\sqrt{2}, & n=2k \\ -1/\sqrt{2}, & n=2k+1 \\ 0, & \text{otherwise} \end{cases}$$

It follows that

$$\varphi_{2k}[n] = \varphi_0[n-2k],$$

$$\varphi_{2k+1}[n] = \varphi_1[n-2k]$$

The transform is

$$X[2k] = \langle \varphi_{2k}, x \rangle = \frac{1}{\sqrt{2}} (x[2k] + x[2k+1]),$$

The reconstruction is obtained from

$$X[2k+1] = \langle \varphi_{2k+1}, x \rangle = \frac{1}{\sqrt{2}} (x[2k] - x[2k+1])$$

$$x[n] = \sum_{k \in \mathbb{Z}} X[k] \varphi_k[n/\sqrt{2}]$$

Discrete Wavelet Transform

1) Split Step:

$$d_i^0 = x_{2i+1}$$

$$s_i^0 = x_{2i} \quad (3)$$

2) Lifting Step:

$$d_i^1 = d_i^0 + \alpha \times (s_i^0 + s_{i+1}^0) \quad (4)$$

$$s_i^1 = s_i^0 + \beta \times (d_{i-1}^1 + d_i^1) \quad (5)$$

$$d_i^2 = d_i^1 + \gamma \times (s_i^1 + s_{i+1}^1) \quad (6)$$

$$s_i^2 = s_i^1 + \delta \times (d_{i-1}^2 + d_i^2) \quad (7)$$

3) Scaling Step:

$$d_i = \frac{1}{K} \times d_i^2$$

$$s_i = K \times s_i^2 \quad (8)$$

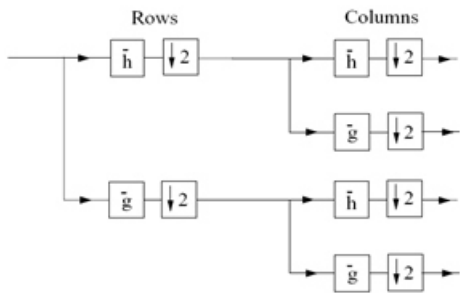


Fig3. 2-D Analysis Filter Bank

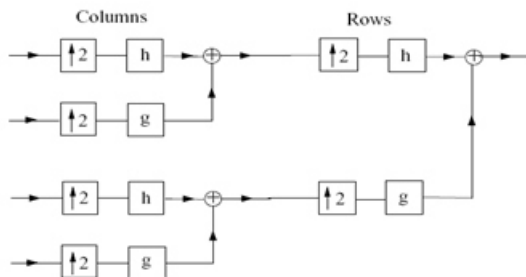


Fig4. 2-D Synthesis Filter Bank

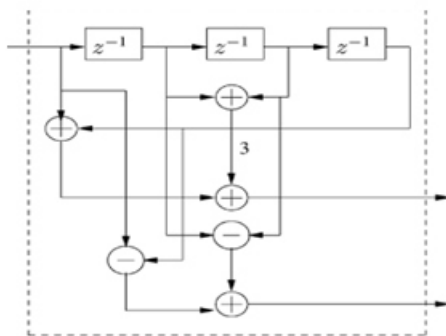


Fig5. Harr 4 AI Filter Structure

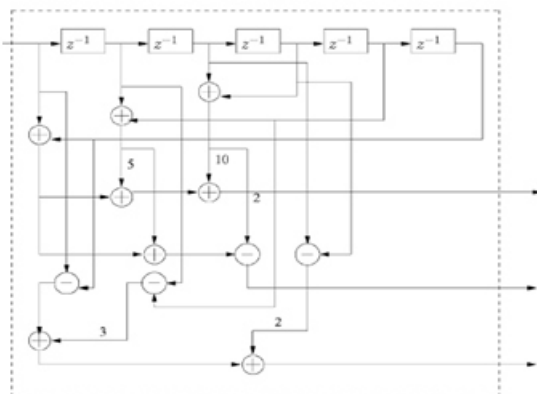


Fig6. Harr 4 AI Filter Structure

V.SIMULATION RESULTS:

The architectures for haar-4 and -6 filter banks were implemented on Xilinx Spartan-6 by using the Xilinx 14.3 and tested using different designs. Gray 512x512 images Haar-4 filter banks whereas Mandrill, Lena, and CT head were submitted to the Haar-6 filter banks. Hardware results were crosschecked with MATLAB. Fig. 7 displays hardware results from the Xilinx FPGA for the haar-4 and -6 filter banks.

Table VI shows proposed Harr-4 and -6 architectures performance comparison for decomposition of single level with 8-bit Lena image. For comparison, we devised a version of the proposed system that operates over fixed-point arithmetic instead of AI-based arithmetic. For such, we employed 8 bits for word size with 6 fractional bits. In this case, the required filter banks were implemented by quantizing the exact filter coefficients into the fixed-point representation.

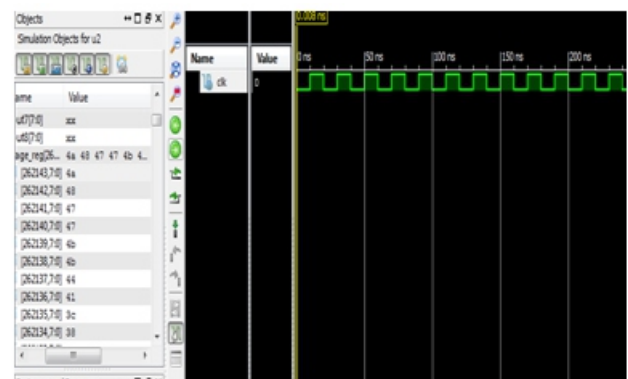


Fig7. Simulation of input image.



Fig8. Decomposition of input image using Haar wavelet filter bank

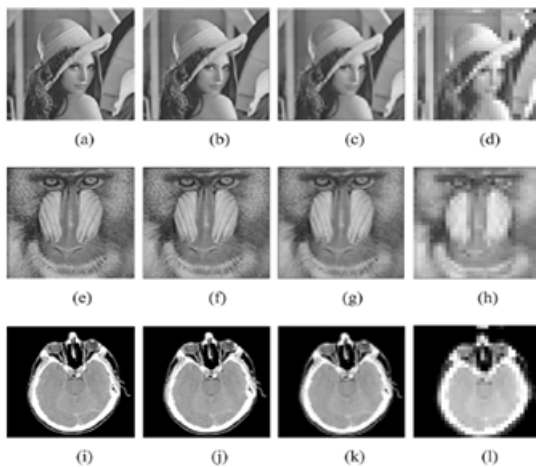


Fig9. Approximation sub-images, and obtained from FP Scheme (8-bit word length and 6 fractional bits) Haar 4- and 6-tap wavelet filters.

**TABLE I
COMPARISON OF DAUB-4 AND -6 PERFORMANCES:**

Facet	Haar-4	Haar-6
Device	xc6slx16-cgs324	xc6slx16-cgs324
Operating Freq	398.23 MHZ	314.69 MHZ
Static power	35 mW	47mW
PSNR	62.7	65.83
Adders	32	61
Registers	258	765
CPD	2.23 ns	3.27 ns

VI.CONCLUSION:

We proposed a novel approach to constrict the DWT with multiplier less design and more over the design less effective for the quantization. We designed poly-encoded AI-based 2-D wavelet filter which offers high numeric. The resultant DWT provides the improvement in PSNR and SNR. We designed the Haar-4 and Haar-6 architectures and we characterize the performance evaluation and we concluded that the Haar-6 will give good performance.

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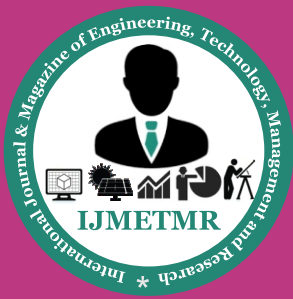
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