

Reducing Timing Jitter in OFDM Systems Using Oversampling

J. Harshitha

M.Tech in CESP,

RVR & JC College of Engineering,

Chandramoulipuram, Chowdavaram, Guntur Dist,
Andrapradesh, India.

N. Renuka

Associative Professor,

Dept of ECE,

RVR & JC College of Engineering,
Chandramoulipuram, Chowdavaram, Guntur Dist,
Andrapradesh, India.

ABSTRACT:

Timing jitter is a major issue in very high speed OFDM systems, which causes serious impairments that, degrades the performance of overall system. In order to reduce the effect caused by timing jitter, here in this proposal, we introduced a novel scheme that reduces the noise caused by the effect of timing jitter by using integral oversampling under different channel environments such as AWGN, Rayleigh and Rician channels. Simulation results have shown that the average jitter noise which is affected by timing jitter occurrence has reduced under various channel conditions. Also compared the performance of channels in terms of sub carrier index versus powerIndex Terms: OFDM, Timing Jitter, Integral oversampling, AWGN, Rayleigh and Rician channels.

I.INTRODUCTION:

Due to the multi path fading effect and receiver complexity of wireless channels, the traditional modulation techniques which are based on single carrier can achieve limited data rates. In many wireless multimedia network applications [1], the high data-rates were desirable. However, the symbol duration will get reduced whenever there is an increment in communication systems data rates. Therefore, the communication systems using single carrier modulation suffer from severe inter symbol interference (ISI) caused by dispersive channel impulse response, thereby needing a complex equalization mechanism. Orthogonal Frequency Division Multiplexing (OFDM) is a special form of multi carrier modulation technique, in which the total frequency selective fading channel will be divided into many orthogonal narrow band flat fading sub channels. In OFDM system high-bit-rate data stream is transmitted in parallel over a number of lower data

rate subcarriers and do not undergo ISI due to the long symbol duration[2]. Major advantages of OFDM systems are

- » High spectral efficiency
- » Simple digital realization by using the FFT operation
- » Due to the ISI avoidance, the complexity in the receiver will be reduced
- » Various modulation schemes will be used to achieve the best performance of the system

Due to the above mentioned advantages, OFDM has been used in many wireless applications such as Wireless Personal Area Network (WPAN), Wireless Metropolitan Area Network (WMAN), Digital Audio Broadcasting (DAB) [3], Wireless Local Area Network (WLAN) [4] and Digital Video Broadcasting (DVB) [5]. It is also useful for 3GPP-LTE, IEEE 802.20 and 802.16 [6], [7]. With the use of cyclic prefix for eliminating the effect of ISI, there is a need for a simple one tap equalizer at the OFDM receiver. OFDM brings in unparalleled bandwidth savings, which leads to high spectral efficiency.

In Recent years optical systems uses OFDM for higher data rates (see [8] and the references therein) since, data rates in fiber optic systems are typically much higher than the Radio Frequency (RF) wireless systems. Timing jitter is a major problem and it cause serious impairments at these very high data rates, which in results the poor efficiency and degraded performance of the system. Sampling clock is the major source of jitter in the very high speed analog-to-digital converters (ADCs) which are required in these systems. It causes many serious issues in OFDM radios which uses high frequency band pass sampling [9]. In [10] and [11-15] authors analyzed the effect of timing jitter. These papers focus on the colored low pass timing jitter which is typical of systems using phase lock loops (PLL).

They consider only integral oversampling. By leaving some band- edge sub carriers which will be unused, the fractional oversampling can be achieved in OFDM. Here in this proposal we investigated the integral oversampling to reduce the timing jitter. Very high speed ADCs typically uses parallel pipeline architecture not a PLL [13] and for these the white jitter which is the focus of this paper is a more appropriate model.

SYSTEM MODEL:

Fig.1 shows the block diagram of a high speed OFDM system, T denotes the OFDM symbol period, in each T up to N complex values representing the constellation points are used to modulate up to N sub carriers. In Practical OFDM systems, the jitter can be introduced at a number of points. But, here in this proposal we considered that the jitter is introduced at sampler block of the receiver ADC. The definition of timing jitter has been shown in fig.2. Ideally the OFDM signal is sampled at uniform intervals of T/N . In fig.2 (a) the uniform sampling intervals has been represented by dashed lines, where the actual sampling times represented by solid lines. The deviation between the actual sampling times and uniform sampling times is caused by the effect of timing jitter, it denoted by τ_n . Discrete timing jitter τ_n example is shown in fig.2 (b).

In [5], [14] and [15] we showed that the timing jitter matrix can describe the effect of timing jitter in OFDM systems. The construct matrix form for OFDM with timing jitter is

$$Y = WHX^T + N \tag{1}$$

Where,

Y = received signal

X = transmitted signal

N = Additive white Gaussian signal

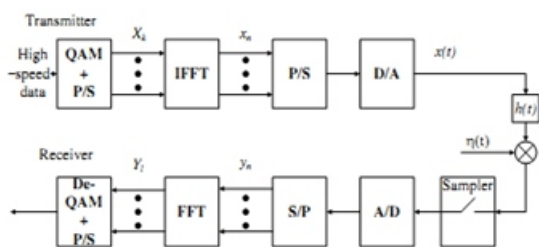


Fig.1. Block diagram of general High Speed OFDM System

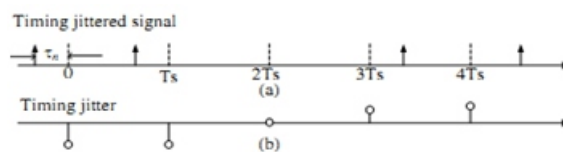


Fig.2. Definition of timing jitter

H is channel response matrix and W is the timing jitter matrix

$$Y = [Y_{(-N)/2+1} \dots Y_0 \dots Y_{(N)/2}]^T$$

$$X^T = [X_{(-N)/2+1} \dots X_0 \dots X_{(N)/2}]^T$$

$$H = \text{diag}(H_{(-N)/2+1} \dots H_0 \dots H_{(N)/2})$$

And

$$W = \begin{bmatrix} w_{-\frac{N}{2}+1, -\frac{N}{2}+1} & \dots & w_{-\frac{N}{2}+1, 0} & \dots & w_{-\frac{N}{2}+1, \frac{N}{2}} \\ \vdots & \ddots & \vdots & \ddots & \vdots \\ w_{0, -\frac{N}{2}+1} & \dots & w_{0, 0} & \dots & w_{0, \frac{N}{2}} \\ \vdots & \ddots & \vdots & \ddots & \vdots \\ w_{\frac{N}{2}, -\frac{N}{2}+1} & \dots & w_{\frac{N}{2}, 0} & \dots & w_{\frac{N}{2}, \frac{N}{2}} \end{bmatrix} \tag{2}$$

Timing jitter causes an added noise like component in the received signal.

$$Y = HX^T + (W - I)HX^T + N \tag{3}$$

Where I is the $N \times N$ identity matrix. In eq. (3), the first term represents wanted term while the second term indicates jitter noise. In it was shown that the elements of the timing jitter matrix W are given by

$$w_{l,k} = \frac{1}{N} \sum_{n=-N/2+1}^{N/2} e^{j2\pi k \frac{\tau_n}{T}} e^{j\frac{2\pi}{N}(k-l)n} \tag{4}$$

Where n is the time index, k is the transmitted subcarrier index and l is the received subcarrier index.

ANALYSIS OF TIMING JITTER:

We now analyze the effect integral oversampling in OFDM and show that it will reduce the impairments caused by timing jitter. The received signal will be sampled with MN/T to achieve the integral oversampling, where M is an integer. Some band edge sub carriers are unused in the transmitted signal for fractional oversampling. The band width of

the OFDM signal is $N/2T$, when all N sub carriers are modulated. So as shown in fig.2 sampling at interval of Nyquist sampling T/N . The bandwidth of the signal is $\frac{N_L+N_U}{2T}$, if instead, only sub carriers with indices between $-N_L$ and N_U are non zero. In this case the sampling interval is above Nyquist rate. In general, where both integral and fractional oversampling is applied, the signal samples after the ADC receiver are given by,

$$y_{n_M} = y\left(\frac{n_M T}{NM}\right) = \frac{1}{\sqrt{N}} \sum_{k=-N_L}^{N_U} H_k X_k e^{j2\pi k \frac{n_M T}{NM}} + \eta\left(\frac{n_M T}{NM}\right) \quad (5)$$

Where n_M = the time index of discrete over sampling and η is the AWGN. The N -point FFT in the receiver is replaced by an 'oversized' NM -point FFT in proposed algorithm. The output of this FFT is a vector of length NM with elements

$$Y_{l_M} = \frac{1}{\sqrt{N}} \frac{1}{\sqrt{NM}} \sum_{n_M=-\frac{NM}{2}+1}^{\frac{NM}{2}} y_{n_M} e^{(-j2\pi n_M \frac{l_M}{NM})} \quad (6)$$

Where l_M is the NM -point FFT output index. Then combining the equations (4), (5) and (6), the modified coefficients for the proposed case is obtained by,

$$W_{l_M, k} = \frac{1}{NM} \sum_{n_M=-\frac{NM}{2}+1}^{\frac{NM}{2}} e^{j2\pi k \frac{n_M}{T}} e^{j\frac{2\pi}{NM}(k-l_M)n_M} \quad (7)$$

We now calculate the each sub carrier average jitter noise power for the case of white jitter. From (3) and (6),

$$Y_{l_M} = H_{l_M} X_{l_M} + \sum_{k=-N_L}^{N_U} (W_{l_M, k} - I_{l_M, k}) H_k X_k + N(l) \quad (8)$$

Where the second term represents the jitter noise. In the following, we consider a flat channel with $H_k = 1$, and consider that the power of transmitted signal has been distributed equally across the used subcarriers so that for each used subcarrier $E\{X_k^2\} = \sigma_s^2$. Then the average jitter noise power, $P_j(l)$ to received signal power of l_{th} subcarrier is given by,

$$\frac{P_j(l)}{\sigma_s^2} = \frac{E\left\{\left|\sum_{k=-N_L}^{N_U} (W_{l, k} - I_{l, k}) X_k\right|^2\right\}}{\sigma_s^2} = \sum_{k=-N_L}^{N_U} E\left\{|W_{l, k} - I_{l, k}|^2\right\} \quad (9)$$

For a system in which there are equal number of unused subcarriers at each band edge, so $N_U = N_v/2$, $-N_L = -N_v/2 + 1$

Where N_v is number of used subcarriers. Then the eq.(9) becomes

$$\frac{P_j(l)}{\sigma_s^2} = \sum_{k=-N_v/2+1}^{N_v/2} E\left\{|W_{l, k} - I_{l, k}|^2\right\} \quad (10)$$

Reducing the numbers of subcarriers will results in reduction of over all data, the transmitted power and even bandwidth when the symbol period in unchanged. To make a fair comparison the symbol period is also reduced so that $T_v = N_v T_N / N$, where T_v is N_v used sub carriers symbol period and T_N is the symbol period of N used subcarriers.

$$\frac{P_j(l)}{\sigma_s^2} = \sum_{k=-N_v/2+1}^{N_v/2} \frac{1}{NM} \left(\frac{2\pi k}{T_v}\right)^2 E\{T_n^2\} \quad (11)$$

Using $\sum_{k=-N_v/2+1}^{N_v/2} k^2 = \frac{1}{12} N_v (2 + N_v^2)$, we

obtain

$$\frac{P_j(l)}{\sigma_s^2} = \frac{2}{3M} E\{\tau_n^2\} \left(\frac{N_v}{N}\right) \left(\frac{\pi}{T_v}\right)^2 + \frac{1}{3M} E\{\tau_n^2\} \left(\frac{N_v^3}{N}\right) \left(\frac{\pi}{T_v}\right)^2 \quad (12)$$

Since the first term in eq. (12) is very small, so it can be ignored. Then the equation will be re written as

$$\frac{P_j(l)}{\sigma_s^2} = \frac{\pi^2}{3M} E\{\tau_n^2\} \left(\frac{N_v N}{T_N^2}\right) \quad (13)$$

If we consider that there is no oversampling then $M=1$ and $N_v = N$, therefore

$$\frac{P_j(l)}{\sigma_s^2} = \frac{\pi^2}{3} E\{\tau_n^2\} \left(\frac{N^2}{T_N^2}\right) \quad (14)$$

Comparing the eq. (13) and (14), it can be seen that the combination of integral and fractional oversampling reduces the jitter noise power by a factor of N_v/NM .

IV.SIMULATION RESULTS:

In this section we present the simulations, which have been tested in MATLAB 2011a version. Here we had considered 2000 OFDM symbols, $N = 512$ subcarriers and $E\{\tau_n^2\} = (0.3T_N/N)^2$ jitter variance, which will not vary when we apply oversampling. Fig.3 shows that the variance of the noise due to the jitter as a function of index of received subcarriers when band-edge subcarriers are unused, which shows that the index of subcarrier and that removing the band-edge subcarriers reduces the noise equally across the all subcarriers. Fig.4 shows that the simulation and theoretical results of average jitter noise power as a function of the oversampling factor. When you observe the graph, we can conclude that for every doubling of sampling rate reduces the jitter noise power by 3dB.

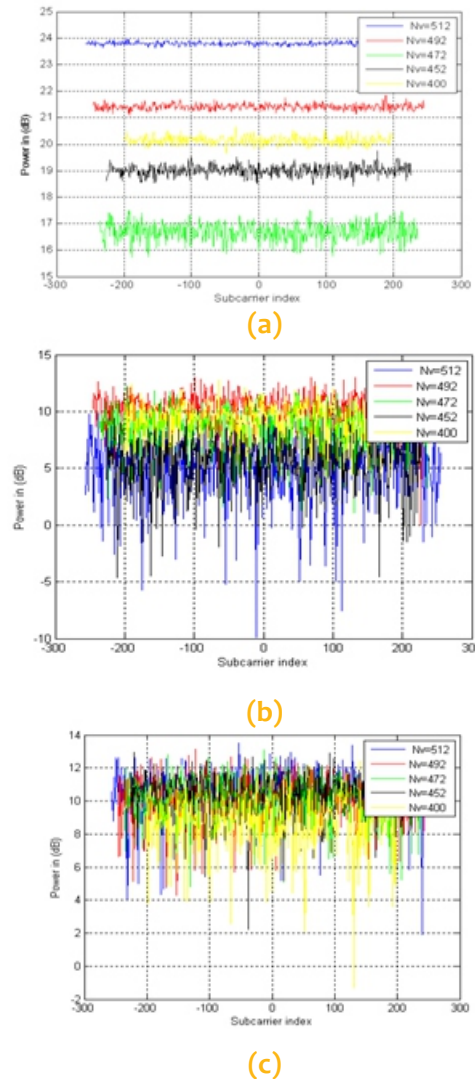
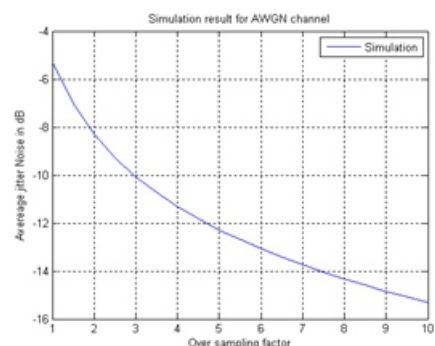


Fig.3 (a) Received power versus subcarrier index under AWGN channel (b) Under Rayleigh channel (c) Under Rician channel



(a)

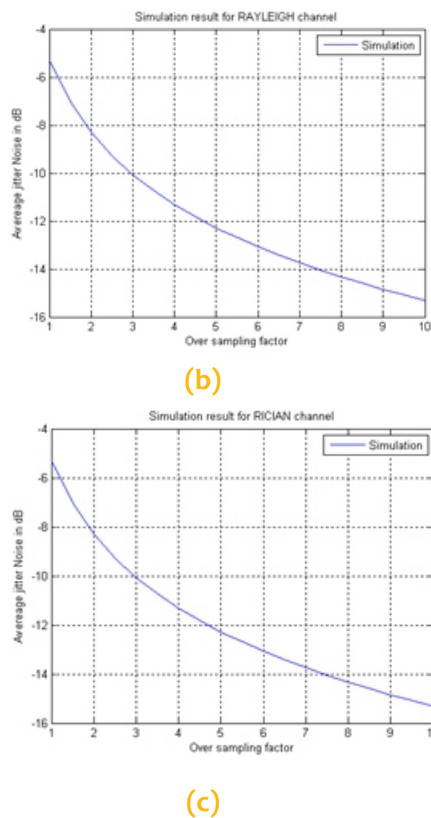


Fig.4 (a) Average jitter noise versus Oversampling factor under AWGN, (b) Under Rayleigh channel, (c) Under Rician channel

V.CONCULSIONS :

It has been shown that the proposed algorithm for timing jitter noise power reduction shows better performance and it reduces the impairments caused by the timing jitter in OFDM systems. In this, we investigated oversampling mathematically by deriving the equations to them in the proposed oversampling technique, which was implemented by receiver sampling rate increment. Finally, it shows that the proposed algorithm has given reduced noise power of 3dB for every doubling of sampling rate under various channel conditions such as AWGN, Rayleigh and Rician.

REFERENCES:

1.Wu Y., W. Y. Zou, "Orthogonal frequency division multiplexing: A multi-carrier modulation scheme," IEEE Transactions on Consumer Electronics, vol. 41, no. 3, pp.392-399, Aug. 1995.

2.Van Nee R., Prasad R., OFDM for wireless Multimedia Communications, Artech House, 2003.

3.ETSI, "Radio broadcasting systems; Digital Audio Broadcasting (DAB) to mobile, portable and fixed receivers", European Telecommunication Standard, Standard EN-300401, May 1997.

4.Hiperlan2, "Broadband Radio Access Networks (BRAN), HIPERLAN Type 2; Physical (PHY) layer", ETSI, Tech. Rep., 1999.

5.ETSI, "Digital Video Broadcasting (DVB); Framing structure, channel coding and modulation for digital terrestrial television", European Telecommunication Standard, Standard EN-300-744, 2004-2006.

6.Part 16: Air Interface for Fixed Broadband Wireless Access Systems Amendment 2: "Medium Access Control Modifications and Additional Physical Layer Specifications for 2-11 GHz", IEEE, Standard IEEE Std. 802.16a-2003, 2003.

7.Part 16: Air interface for fixed broadband wireless access systems. "Amendment for physical and medium access control layers for combined fixed and mobile operation in licensed bands," IEEE, Standard IEEE Std 802.16e/D12, October 2005.

8.J. Armstrong, "OFDM for optical communications," J. Lightwave Technol., vol. 27, no. 1, pp. 189-204, Feb. 2011.

9.V. Syrjala and M. Valkama, "Jitter mitigation in high-frequency band pass sampling OFDM radios," in Proc. WCNC 2012, pp. 1-6.

10.K. N. Manoj and G. Thiagarajan, "The effect of sampling jitter in OFDM systems," in Proc. IEEE Int. Conf. Commun., vol. 3, pp. 2061-2065, May 2003.

11.U. Onunkwo, Y. Li, and A. Swami, "Effect of timing jitter on OFDM based UWB systems," IEEE J. Sel. Areas Commun., vol. 24, pp. 787-793, 2006.

12.L. Yang, P. Fitzpatrick, and J. Armstrong, "The Effect of timing jitter on high-speed OFDM systems," in Proc. AusCTW 2009, pp. 12-16.

13.L. Sumanen, M. Waltari, and K. A. I. Halonen, "A 10-bit 200-MS/s CMOS parallel pipeline A/D converter," IEEE J. Solid-State Circuits, vol. 36, pp. 1048-55, 2010.

14.Ch Pavan kumar and Poornima Padaraju "Jitter Noise Power Reduction in OFDM by Oversampling", International Journal of Engineering Research and Applications (IJERA), Vol. 2, No. 4, pp.1810-1813, 2012.

15.Monika Tiwari, Kanwar Preet Kaur, "Analysis on Lowering the Effect of Timing Jitter in OFDM System using Oversampling", International Journal of Engineering and Advanced Technology (IJEAT), Vol. 3, No. 5, 2014.