

An Optimum power control and current balancing scheme for High-Power LED Lighting.



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Abstract: High-power LEDs (HPLEDs) or highoutput LEDs (HO-LEDs) can be driven at currents from hundreds of mA to more than an ampere, compared with the tens of mA for other LEDs. Some can emit over a thousand lumens. LED power densities up to 300 W/cm2 have been achieved. Since overheating is destructive, the HPLEDs must be mounted on a heat sink to allow for heat dissipation. If the heat from a HPLED is not removed, the device will fail in seconds. This paper proposes a PWM current balancing scheme which adaptively minimizes the on-time current within the PWM averaged-current control to optimize the LED luminous efficacy. Applying an independent current balancing control in each LED string often increases the control complexity, but the precise dimming can be easily incorporated into this scheme without additional circuitry. This paper provides solution as a better current balancing scheme with optimum power utilization compared to previous methodologies.

Keywords: LEDs, Power, PWM, Heating, Current balancing, high luminous efficiency.

Introduction:

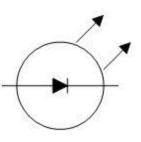
A light emitting diode (LED) is essentially a PN junction opto-semiconductor that emits a monochromatic (single color) light when operated in a forward biased direction. LEDs convert electrical energy into light energy. They are frequently used as

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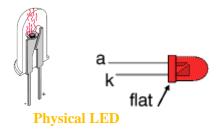
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"pilot" lights in electronic appliances to indicate whether the circuit is closed or not.



Schematic of LED

The most important part of a light emitting diode (LED) is the semi-conductor chip located in the center of the bulb as shown at the right. The chip has two regions separated by a junction. The p region is dominated by positive electric charges, and the n region is dominated by negative electric charges. The junction acts as a barrier to the flow of electrons between the p and the n regions. Only when sufficient voltage is applied to the semi-conductor chip, can the current flow, and the electrons cross the junction into the p region.



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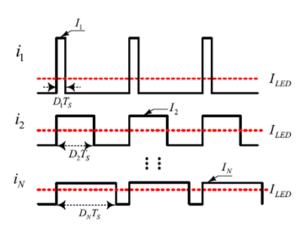
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Concept of The Current Balancing

The concept of the current balancing scheme using PWM is to maintain a constant averaged LED current via the tuning of the duty cycle of each LED string shown in Fig, where N LED strings are connected to a common output terminal with dc bus voltage Vo, which is used to control the duty cycle of each switching regulator to provide the required power of each LED string.

Denote the instantaneous current of each LED string as in which is modulated by the current balancing switch with on time current In , where n = 1, 2, ..., N. The current waveforms are illustrated in Fig below with a common averaged current of ILED.

To achieve current balancing, the turn-on current amplitude In and turn-on duty cycle Dn of string n should satisfy



 $I_{LED} = InDn, n = 1, 2, ..., N$

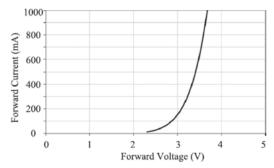
Current waveform in each LED string with PWM

In the subsequent analysis, Xn will be used to describe the property X of LED-string n, where n = 1, 2, ..., N. From (1), In is determined by VO and the characteristic of the LED string.

The PWM current balancing scheme controls a constant ILED by adjusting Dn. Intuitively, VO must adjust to be no smaller than the accumulative voltage drop of any LED string to guarantee sufficient on-time current. LEDs are nonlinear components with the

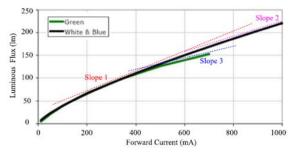
typical exponential V–I curves shown in Fig below. A slight voltage difference in Vo will result in a large ontime current variation in In.

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Typical forward voltage versus forward current of Cree Xlamps XR-E LEDs at $Tj = 25 \circ C$

A typical relationship of the luminous flux versus the driving current is illustrated in Fig above. The emitted luminous flux is not linear with the driving current and slowly decreases when the driving current is increasing due to higher junction temperature as a result of more heat dissipation.



Typical luminous flux versus forward current of Cree Xlamps XR-E LEDs at $Tj = 25 \circ C$

Normally, the normalized forward voltage varies insignificantly compared with the normalized forward current, and thus can be assumed constant. Therefore, the slope at a particular point of the curve in Fig above can reflect the trend of luminous efficacy = (luminous flux)/(VLEDILED))at that specific position.

Therefore, the maximum of Dn is unity and the string with unity Dn has best luminous efficacy among other LED strings having duty cycle smaller than unity. Hence, Vo should better be just above the maximum of

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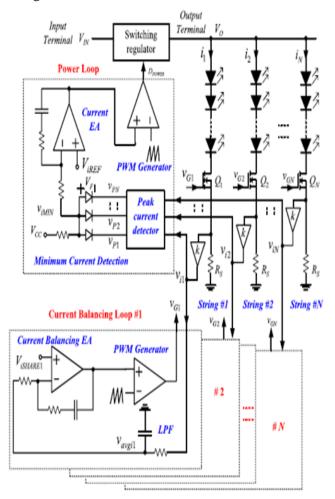
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all the LED-string voltage drops to produce a set of ILED with maximized Dn (n = 1, 2, ..., N) and minimized In (n = 1, 2, ..., N). Then, the high luminous efficacy of LED sources is achieved.

Circuit Design And Implementation

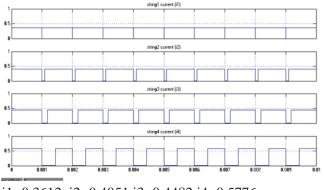
From the aforementioned analysis, independent PWM controls for light dimming and current balancing are required in each LED string. Moreover, the adaptive Vo regulation is realized by the proposed minimum on-time current control via the main switching regulator. There are two cooperative control loops, one being the N-LED-string current balancing loop and the other being the power loop for regulating the terminal voltage Vo.



Schematic of power loop control and current balancing loop control in Each LED string

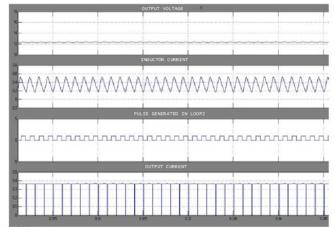
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Results Of Individual Current Balancing Loop

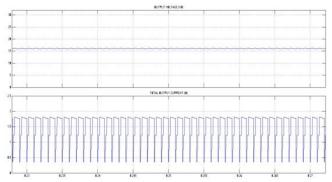


i1=0.3612 i2=0.4051 i3=0.4482 i4=0.5776 D1 = 0.99 D2 = 0.9 D3 = 0.8 D4 = 0.6i1D1 = 0.3612 i2D2=0.3645 i3D3=0.3585 i4D4=0.3465

Results Of Single String



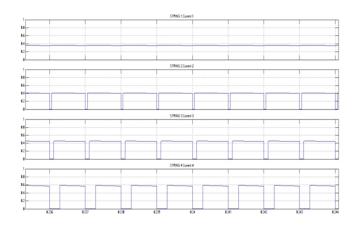
Results For Minimum Current Of I_{MIN}= 0.3594A



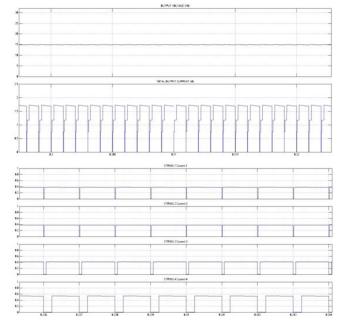
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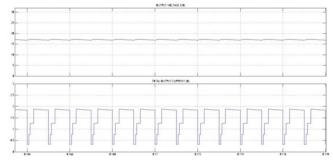
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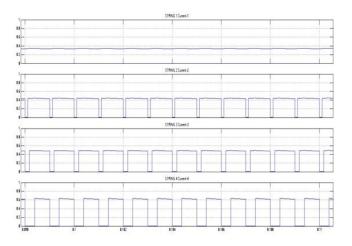


Results For Minimum Current Of I_{MIN}= 0.3696A



Results For Minimum Current Of I_{MIN}= 0.3368A





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Conclusion

Current balancing techniques are important in driving multiple LEDs due to device variation and heterogeneous working environment. This project proposes a PWM current balancing scheme which uses an optimal feedback control for maximizing luminance efficacy. Detailed design, analysis, and implementation are provided in this project. The performance of the above proposed LED driver system is evaluated using simulation in MATLAB software.

References:

[1] Xiaohui Qu, Member, IEEE, Siu-Chung Wong, Senior Member, IEEE, and Chi K. Tse, Fellow, IEEE, A Current Balancing Scheme With High Luminous Efficacy for High-Power LED Lighting, IEEE TRANSACTIONS ON POWER ELECTRONICS, VOL. 29, NO. 6, JUNE 2014

[2] C. Y. Wu, T. F. Wu, J. R. Tsai, Y. M. Chen, and C. C. Chen, "Multi string LED backlight driving system for LCD panels with color sequential display and area control," IEEE Transactions Industrial Electron., vol. 55, no. 10, pp. 3791–3800, Oct. 2008.

[3] H. J. Chiu and S. J. Cheng, "LED backlight driving system for large scale LCD panels," IEEE Trans. Ind. Electron., vol. 54, no. 5, pp. 2751–3760, Oct. 2007.

[4] Q. Hu and R. Zane, "Minimizing required energy storage in off-line LED drivers based on series-input



converter modules," IEEE Trans. Power Electron., vol. 26, no. 10, pp. 2887–2895, Oct. 2011.

[5] K. I. Hwu and Y. T. Yau, "Applying onecomparator counter-based sampling to current sharing control of multi channel LED strings," IEEE Transactions Industrial Applications., vol. 47, no. 6, pp. 2413–2421, Nov./Dec. 2011.

[6] D. Liu, A. Hu, G. Wang, and W. Hu, "Current sharing schemes for multiphase interleaved DC/DC converter with FPGA implementation," in Proc. IEEE Int. Conf. Electr. Control Engineering., 2010, pp. 3512

[7] X. Qu, S. C. Wong, and C. K. Tse, "Non-cascading structure for electronic ballast design for multiple LED lamps with independent brightness control," IEEE Transactions Power Electronics., vol. 25, no. 2, pp. 331–340, Feb.2010.

[8] W. Chen and S. Y. R. Hui, "A dimmble lightemitting diode driver with mag-amp postregualtors for multistring applications," IEEE Transactions Power Electronoics ., vol. 26, no. 6, pp. 1714–1722, Jun. 2011.

[9] J. Zhang, L. Xu, X. Wu, and Z. Qian, "A precise passive current balancing method for multioutput LED drivers," IEEE Trans. Power Electron., vol. 26, no. 8, pp. 2149–2159, Aug. 2011.

[10] K. I. Hwu and S. C. Chou, "A simple currentbalancing converter for LED lighting," in Proc. IEEE Appl. Power Electron. Conf., 2009, pp. 587–590.

[11] X. Wu, Z. Wang, and J. Zhang, "Design considerations for dual-output quasi-resonant flyback LED driver with current-sharing transformer," IEEE Trans. Power Electron., vol. 28, no. 10, pp. 4820–4830, Oct. 2013.

[12] K. H. Jung, J. W. Yoo, and C. Y. Park, "A design of current balancing circuit for parallel connected LED strings using balancing transformers," in Proc. IEEE Int. Conf. Power Electron.- ECCE Asia, 2011, pp. 528–535.