

Improved Accuracy of Sharing Reactive Power by Error Reduction in Islanded Micro grid



N.Praneeth

Assistant Professor,
Dept of EEE,
Mother Theresa Institute
of Science & Technology,
Telangana, India.



K.Navarani

Dept of EEE,
Mother Theresa Institute
of Science & Technology,
Telangana, India.



K.Umadevi

Dept of EEE,
Mother Theresa Institute
of Science & Technology,
Telangana, India.



K.Venkata Aparna

Dept of EEE,
Mother Theresa Institute
of Science & Technology,
Telangana, India.

Abstract:

Droop control method is one of method that tries to solve the mismatch value of reactive power share of micro grid for islanded operation. As conventional is not meeting its approach for sharing reactive power, this paper proposes an improved performance of droop control technique that it can share reactive power accurately and have error minimization. The proposed method gives two advantage operations error reduction and voltage recovery, by low band width synchronization signals. Where error reduction technique results in decrease in voltage amplitude so voltage recovery is also done as compensation. Simulation results show that the improved droop controller can share load active and reactive power, enhance the power quality of the microgrid, and also have good dynamic performance.

Index Terms:

Droop control, low-bandwidth synchronization signals, microgrid, reactive power sharing, and voltage recovery operation.

I. Introduction:

THE application of distributed generation (DG) has been increasing rapidly in the past decades. Compared to the conventional centralized power generation, DG units have advantages of less pollution, higher efficiency of energy utilization, more flexible installation location, and less power transmission losses.

Most of the DG units are connected to the grid via power electronic converters, which introduces system resonance, protection interference, etc. To overcome these problems, the microgrid concept was first proposed in the US by the Consortium for Electrical Reliability Technology Solutions [1]. Compared to use a single DG unit, microgrid could offer superior power management within the distribution networks. Moreover, the microgrid can operate in grid-connected mode or islanded mode and benefit both the utility and customers in economy [2]–[7]. In an islanded mode, the load power in the microgrid should be properly shared by multiple DG units. Usually, the droop control method which mimics the behavior of a synchronous generator in traditional power system is adopted, which does not need the use of critical communications [8]–[14], [21], [22].

The active power sharing is always achieved by the droop control method easily. However, due to effects of mismatched feeder impedance between the DGs and loads, the reactive power will not be shared accurately. In extreme situations, it can even result in severe circulating reactive power and stability problems [11]. To overcome the reactive power sharing issue, a few improved methods have been proposed. Specifically, there are mainly three approaches to address the effect of the interconnecting line impedance on droop-based control. The first approach is to introduce the virtual output impedance by modifying the output voltage reference based on output current feedback [11], [13], [14], [23]. This method can reduce the reactive power sharing error by reducing the relative error of the output impedances.

However, the introduction of the virtual impedance may lead to degradation of the system voltage quality. The second approach is based on a signal injection technique. In [15], a certain harmonic signal containing reactive power information is injected into the output voltage reference of each DG unit, and the output reactive power is regulated to improve the accuracy of the reactive power sharing according to the harmonic power. However, this method results in output voltage distortion.

In [16], in order to reduce the reactive power sharing errors, the method injects some small disturbance signals containing reactive power information into the frequency reference of each DG unit. By using the active power error before and after the injecting signal, this method can eliminate the reactive power sharing error. However, this method is a classic event-triggered control and its stability is not easy to be guaranteed. Additionally, the third approach is usually based on constructed and compensated method. In [17], the method constructs an integral control concerning the common bus voltage to ensure the reactive power sharing. However, in practical situation, the common bus voltage information is difficult to get.

In this paper, a new reactive power sharing method is proposed. The method improves the reactive power sharing by changing the voltage bias on the basis of the conventional droop control, which is activated by a sequence of synchronization events through the low-bandwidth communication network. It is a cost-effective and practical approach since only a low-bandwidth communication network is required. Simulation and experimental results are provided to verify the effectiveness and feasibility of the proposed reactive power sharing method.

This paper is organized as follows: Section II gives the system configuration and the reactive power sharing error analysis with conventional droop control. Section III proposes an improved reactive power sharing control strategy, and the convergence and robustness is analyzed. Simulation and experimental results are given in Section IV. Section V gives the conclusion.

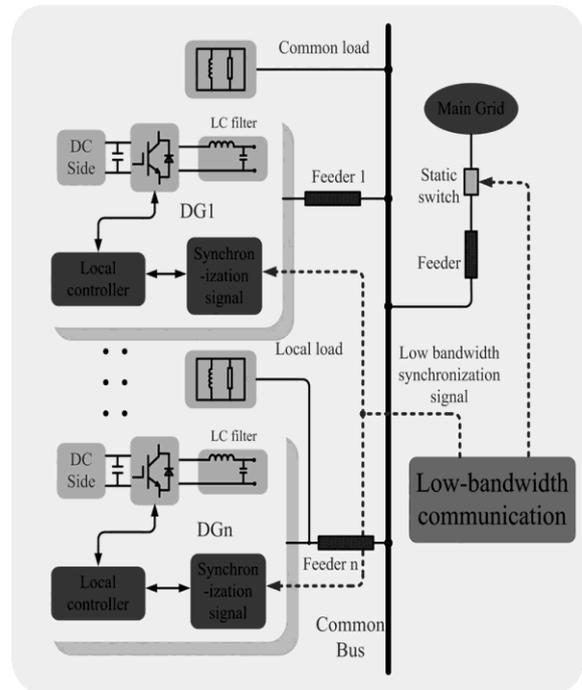


Fig.1. Illustration of the ac micro grid configuration.

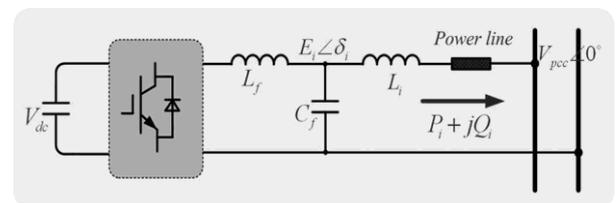


Fig 2. Model of a DG unit.

II. Analysis of Conventional Droop Control Method

A. Configuration and operation of Ac Microgrid:

A classic configuration of a microgrid that consists of multiple DG units and dispersed loads is shown in Fig. 1. The microgrid is connected to the utility through a static transfer switch at the point of common coupling (PCC). Each DG unit is connected to the microgrid through power electronic converter and its respective feeder. This paper aims to solve the fundamental active and reactive power sharing in islanded mode, and the power sharing issue on harmonic currents is out of the scope of this paper.

B. Conventional Droop Control:

Fig. 2 shows the equivalent model of a DG unit, which is interfaced to the common bus of the ac microgrid through a power inverter with an output LCL filter. As shown in Fig. 2, $E_i \angle \delta_i$ is the voltage across the filter capacitor, and $V_{pcc} \angle 0^\circ$ is the common ac bus

voltage. Compared with the inductance of the LCL filter, the line resistance can be ignored. Then, the impedance between inverter and the common bus can be described as X_i ($X_i = \omega L_i$). According to the equivalent circuit in Fig. 2, the inverter output apparent power is S_i , and it can be given by

$$S_i = P_i + jQ_i = \frac{E_i V_{pcc}}{X_i} \sin \delta_i + j \left[\frac{E_i V_{pcc} \cos \delta_i - V_{pcc}^2}{X_i} \right] \quad (1)$$

From (1), the output active and reactive power of the DG units are shown as

$$\begin{cases} P_i = \frac{E_i V_{pcc}}{X_i} \sin \delta_i \\ Q_i = \frac{E_i V_{pcc} \cos \delta_i - V_{pcc}^2}{X_i} \end{cases} \quad (2)$$

Usually, the phase-shift angle δ_i is small. Therefore, the real power P_i and reactive power Q_i of each DG can be regulated by δ_i and the output voltage amplitude E_i , respectively [24]. Then, the conventional droop control is given by

$$\begin{cases} \omega_i = \omega^* - m_i \cdot \overline{P}_i \\ E_i = E^* - n_i \cdot \overline{Q}_i \end{cases} \quad (3)$$

Where ω^* and E^* are the nominal values of DG angular frequency and DG output voltage amplitude, m_i and n_i are the active and reactive droop slopes, respectively. P_i and Q_i are the measured averaged real and reactive power values through a low-pass filter, respectively.

C. Reactive Power Sharing Errors Analysis:

For simplicity, a simplified microgrid with two DG units is considered in this section. According to (2) and (3), the reactive power of the i th DG unit is obtained

$$Q_i = \frac{V_{pcc}(E^* \cos \delta_i - V_{pcc})}{X_i + V_{pcc} n_i \cos \delta_i} \quad (4)$$

Assume the i th and j th DG unit are working in parallel with the same nominal capacity and droop slope. Note that shift angle δ_i is usually very small ($\sin \delta_i \approx \delta_i$, $\cos \delta_i \approx 1$), then the reactive power sharing relative error with respect to Q_i can be expressed as follows:

$$\Delta Q_{err} = \frac{Q_i - Q_j}{Q_i} \approx \frac{X_j - X_i}{X_j + V_{pcc} n_j} \quad (5)$$

It shows that the reactive power sharing relative error is related to some factors, which include the impedance X_j , the impedance difference ($X_j - X_i$), the voltage amplitude V_{pcc} of the PCC, and the droop slope n_j . According to (5), there are two main approaches to improve the reactive power sharing accuracy: increasing impedance X_j and the droop gain n_j . Usually, increasing impedance is achieved by the virtual impedance [11], [13], [14], which requires a high-bandwidth control for inverters. Increasing the droop gain n_j is a simpler way to reduce the sharing error. However, it may degrade the quality of the microgrid bus voltage, and even affects the stability of the microgrid system [18]–[20].

III. Proposed Reactive Power Sharing Error Compensation Method:

A. Proposed Droop Controller:

The proposed droop control method is given as follows:

$$\omega_i = \omega^* - m_i P_i \quad (6)$$

$$E_i(t) = E^* - n_i Q_i(t) - \sum_{n=1}^{k-1} K_i Q_i^n + \sum_{n=1}^k G^n \Delta E \quad (7)$$

where k denotes the time of synchronization event until time t . According to (7), the control is a hybrid system with continuous and discrete traits. In the digital implementation of the proposed method, the continuous variables $E_i(t)$ and $Q_i(t)$ are discretized with sampling period T_s , and T_s is greatly less than the time interval between two consecutive synchronization events. Therefore, the droop (7) at the k th synchronization interval could be expressed as

$$E_i^k = E^* - n_i Q_i^k - \sum_{n=1}^{k-1} K_i Q_i^n + \sum_{n=1}^k G^n \Delta E \quad (8)$$

Where ω^* and E^* are the values of DG angular frequency and output voltage amplitude at no-load condition; m_i and n_i are the droop gains of frequency and voltage of DG- i unit; G_n is the voltage recovery operation signal at the n th synchronization interval, G_n has two possible values: 1 or 0. If $G_n = 1$, it means the voltage recovery operation is performed. Q_{ni} represents the output reactive power of DG- i unit at the n th synchronization interval. K_i is a compensation coefficient for the DG- i unit, ΔE is a constant value for voltage recovery. For simplicity of description, the third term of (8) is referred to the sharing error reduction operation, and the last term is called the voltage recovery operation. For simplicity, the output

voltage for the DG- i unit in (8) is written as follows in iterative method:

$$E_i^k = E_i^{k-1} - n_i(Q_i^k - Q_i^{k-1}) - K_i Q_i^{k-1} + G^k \Delta E \quad (9)$$

Therefore, for its implementation, only E_{k-1} and Q_{k-1} should be stored in DSP. To better understand the proposed method, a specific example is given. If there are two DG units with the same capacity working in parallel, and the conventional droop is only used. There will exist some reactive power sharing error due to some factors. If the sharing error reduction operation for each DG unit is performed at the time, the resulting reactive power sharing error will decrease. The principle behind the sharing error reduction operation can be understood with the aid of Fig. 3. If the aforementioned operation is repeated with time, the reactive power sharing error will converge. However, the associated operations will result in a decrease in PCC voltage. To cope with the problem, the voltage recovery operation will be performed. That is to say if the output voltage of one DG unit is less than its allowed low limit, then the DG unit will trigger the voltage recovery operation until its output voltage is restored to rating value. The output voltage of all the DG units will be added an identical value ΔE to increase the PCC voltage. The idea for the voltage recovery operation can be comprehended with the aid of Fig. 4.

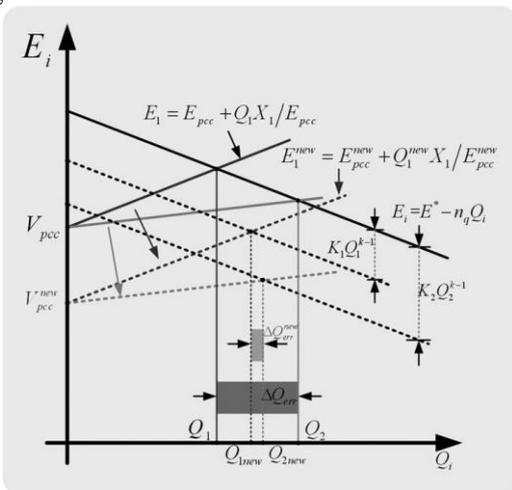


Fig 3. Schematic diagram of the sharing error reduction operation.

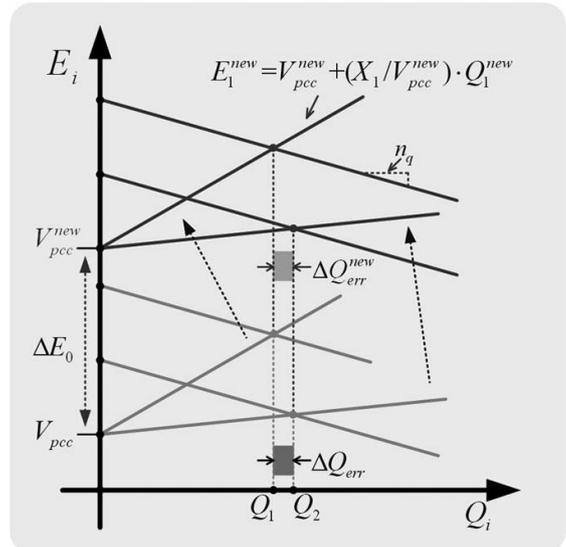


Fig 4. Schematic diagram of voltage recovery mechanism.

B. Communication Setup:

A DG unit can communicate with other DG units by RS232 serial communication. Each DG unit has the opportunity to trigger a synchronization event on the condition that the time interval between two consecutive synchronization events is greater than a permissible minimum value and the output voltage of each DG unit is in the reasonable range. If the output voltage of one DG unit is less than its allowed low limit, it will ask for having the priority to trigger a synchronization event at once. Until the constraint that the interval between two consecutive synchronization events is greater than a permissible minimum value is satisfied, the DG unit with the priority will trigger a synchronization event, and in this event, the command for voltage recovery operation will be sent to other DG units.

If the communication fails (the time interval between two consecutive synchronization events is greater than a permissible maximum value), all the error reduction operations and voltage recovery operations should be disabled and the proposed control method reverts back to the conventional one. According to the aforesaid analysis, such a microgrid system only needs a low-bandwidth communication. And it is robust to the delay of communication. To illustrate this point, the control timing diagram is shown in Fig. 5. The sharing error operation and the voltage recovery operation are performed in update interval.

Sampling operation occurs in sampling interval. There is a time interval τ , which is long enough to guarantee the system being in steady state. It is obvious that proposed method is robust to the time delay because all the necessary operations only need to be completed in an interval, not a critical point.

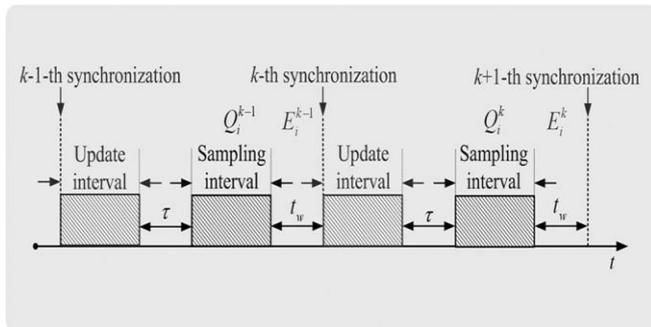


Fig 5. Control timing diagram of one DG with the two consecutive synchronization events.

C. Convergence Analysis:

In this section, the convergence of the proposed method will be proved. Without loss of generality, the sharing reactive power error between DG-*i* and DG-*j* with the same capacity will be analyzed. According to (8), the reactive droop equation for DG-*j* can be expressed as

$$E_j^k = E^* - n_j Q_j^k - \sum_{n=1}^{k-1} K_j Q_j^n + \sum_{n=1}^k G^n \Delta E \quad (10)$$

Subtracting (10) from (8), then

$$\Delta E_{ij}^k = -n \Delta Q_{ij}^k - \sum_{n=1}^{k-1} K \Delta Q_{ij}^n \quad (11)$$

Where $n = n_j = n_i$, $K = K_j = K_i$, and ΔE_{kij} is the voltage magnitude derivation of DG *i* and *j* in the *k*th control period; ΔQ_{kij} is the reactive power sharing error. Similarly, we can get (11) in the (*k*+1)th interval

$$\Delta E_{ij}^{k+1} = -n \Delta Q_{ij}^{k+1} - \sum_{n=1}^k K \Delta Q_{ij}^n \quad (12)$$

Combining (11) and (12), it yields

$$\Delta E_{ij}^{k+1} - \Delta E_{ij}^k = -n \Delta Q_{ij}^{k+1} + n \Delta Q_{ij}^k - K \Delta Q_{ij}^k \quad (13)$$

According to the feeder characteristic, as shown in (2), the following expressions can be obtained:

$$\Delta E_{ij}^{k+1} = \frac{1}{V_{pcc}^{k+1}} (Q_i^{k+1} X_i - Q_j^{k+1} X_j) \quad (14)$$

$$\Delta E_{ij}^k = \frac{1}{V_{pcc}^k} (Q_i^k X_i - Q_j^k X_j) \quad (15)$$

Assume the PCC voltage value satisfy the following:

$$V_{pcc}^{k+1} \approx V_{pcc}^k \approx V \gg 1 \quad (16)$$

Table I: Associated Parameters For Power Stage And Control Of The Dg Unit

Parameters	Values	Parameters	Values
u_{rate} (V)	220	k_{pu}	0.05
L_f (H)	1.5e-3	k_{pi}	50
r_f (Ω)	0.25	K_{ip}	0.2
C_f (μ F)	20	w_c (rad/s)	31.4
L_{Line1} (H)	0.6e-3	m (rad/sec.w)	5e-5
L_{Line2} (H)	0.3e-3	n (v/var)	5e-3
f_s (kHz)	12.8	Ke (v/var)	0.001
f_{rate} (Hz)	50	ΔE_0 (V)	5
T_s (s)	1/12.8e3	T_{syn} (min)(s)	0.1

Subtracting (13) from (14), it yields

$$\Delta E_{ij}^{k+1} - \Delta E_{ij}^k = \frac{X_j}{V} (\Delta Q_{ij}^{k+1} - \Delta Q_{ij}^k) + \frac{\Delta X}{V} (Q_i^{k+1} - Q_i^k) \quad (17)$$

Where, $\Delta X = X_i - X_j$.

Combining the expression (13) and (17), then

$$\Delta Q_{ij}^{k+1} = r \Delta Q_{ij}^k - \frac{\Delta X}{V(n + X_j/V)} [Q_i^{k+1} - Q_i^k] \quad (18)$$

Where, $r = \frac{n + X_j/V - K}{n + X_j/V} < 1$.

According to the contraction mapping theorem, if $|r| < 1$ and $\Delta X = 0$, then reactive power sharing error will converge to zero. Generally, $\Delta X \neq 0$, we should also consider the effect of the second term of (18).

According to the feeder characteristic, as shown in (1), we have

$$Q_i^{k+1} - Q_i^k = \frac{(E_i^{k+1} - E_i^k) V}{X_i} \quad (19)$$

Because of the voltage recovery mechanism, we can ensure $E_{min} \leq E_{ki} \leq E_{max}$ for all *k*

$$|Q_i^{k+1} - Q_i^k| \leq (E_{max} - E_{min}) \frac{V}{X_i} \quad (20)$$

Therefore, the second term of (18) is bounded. According to aforesaid analysis, it can be concluded that the reactive power sharing error is also bounded.

IV. Simulation and Experiments Results

A. Proposed Droop Controller:

The proposed improved reactive power sharing strategy is verified with MATLAB/Simulink and experiment. In the simulations and experiments, a microgrid with two DG systems, as shown in Fig. 1, is employed. The associated parameters for power stage and control of the DG unit are listed in Table I. Also, in order to facilitate the observation of the reactive power sharing, the two DG units are designed with same power rating and different line impedances. The detailed configuration of the single DG unit is depicted in Fig. 6, where a LCL filter is placed between the insulated-gate bipolar transistor bridge output and the

DG feeder. The DG line current and filter capacitor voltage are measured to calculate the real and reactive powers.

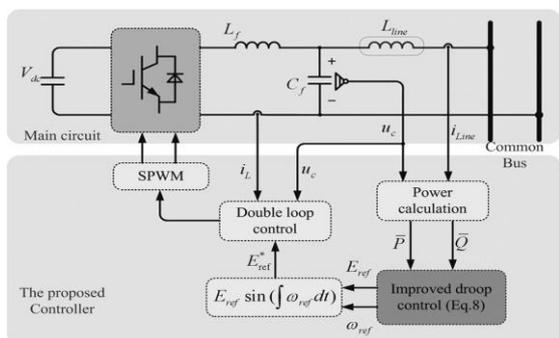


Fig 6. Configuration of one single-phase DG unit.

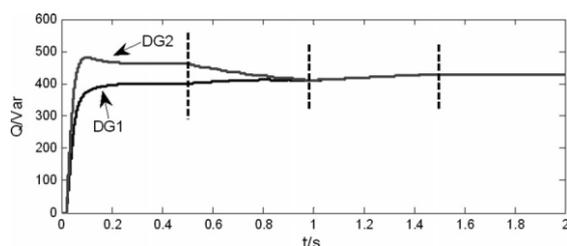


Fig.7. Output reactive powers of two inverters with the improved droop control.

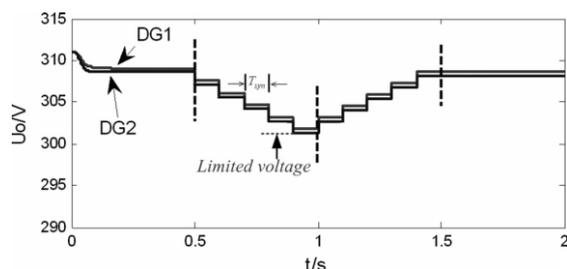


Fig8. Output voltage amplitude of two inverters with the improved droop control.

In addition, the commonly used double closed-loop control is employed to track the reference voltage [5], [7], [12]. 1) Case 1: Power Sharing Accuracy Improvement: Two identical DG units operate in parallel with the proposed voltage droop control. Fig. 7 illustrates the reactive power sharing performance of the two DGs. Before $t = 0.5$ s, the sharing error reduction operation and voltage recovery operation are disabled, which is equivalent to the conventional droop control being in effect.

There exists an obvious reactive power sharing error due to the unequal voltage drops on the feeders. After $t = 0.5$ s, the reactive power sharing error reduction operation is performed, and it is clear that the reactive power sharing error converges to zero gradually. After $t = 1$ s, the voltage recovery operation is performed. It can be observed that the output reactive power increases but the reactive power sharing performance does not degrade. Fig. 8 shows the corresponding output voltages. It can be observed that the output voltages decrease during the sharing error reduction operation, while the voltage recovery operation ensures that DG output voltage amplitudes can restore back nearby to the rated value. The whole process of adjustment can be done steadily in a relatively short period of time. Fig. 9 illustrates active power sharing performance of the two DG units. It is obvious that the proposed improved reactive power sharing strategy does not affect active power sharing performance.

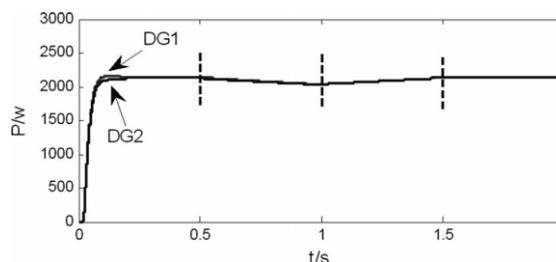


Fig.9 Output active powers of two inverters with the improved droop control.

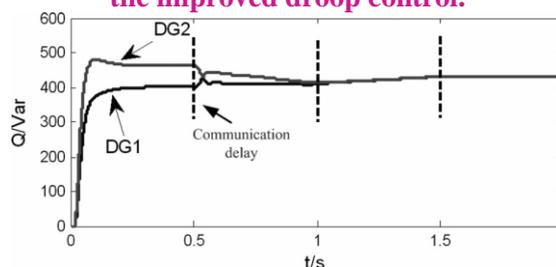


Fig.10. Output reactive powers of the two inverters when 0.02-s time delay occurs in synchronization signal of DG1 unit.

2) Case 2: Effect of the Communication Delay:

To test the sensitivity of the proposed improved droop control to the synchronized signal, a 0.02-s delay is intentionally added to the signal received by DG1 unit at $t = 0.5$ s as shown in Fig. 11, and the simulation results are shown in Figs. 10–12. Compared to the case 1 in Figs. 7 and 9, a small disturbance appears in both the reactive and active power, while the voltage

recovery operations are still able to ensure that the DG unit can deliver the expected reactive power. After $t = 2.0$ s, the active and reactive power sharing errors are almost zero. Therefore, the proposed reactive power sharing strategy is not sensitive to the communication delay. Then, it is illustrated that it is robust to some small communication delays.

3) Case 3: Effect of Load Change:

In order to test the effect of load change with the proposed method, the active load increases about 1.6 kW and the reactive load increases about 0.4 kVar at $t = 2.5$ s, and at $t = 4.5$ s the active load decreases about 3.0 kW and the reactive load decreases about 0.8 kVar. The corresponding simulation results are shown in Figs. 13 and 14. As can be seen, a large reactive power sharing deviation appears at $t = 2.5$ s and $t = 4.5$ s. However, the deviation becomes almost zero after a while. Fig. 15 illustrates the corresponding output voltage waveforms. It can be found that there exist obvious output voltage decrease and output voltage increase processes during each reactive power sharing error reduction process.

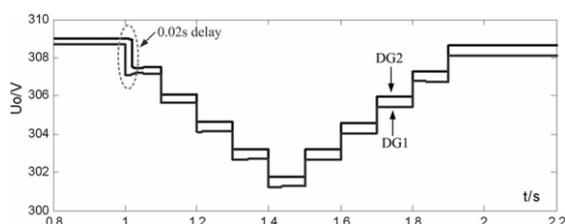


Fig.11. DG output voltage of the inverters when 0.02-s time delay occurs in synchronization signal of DG1 unit.

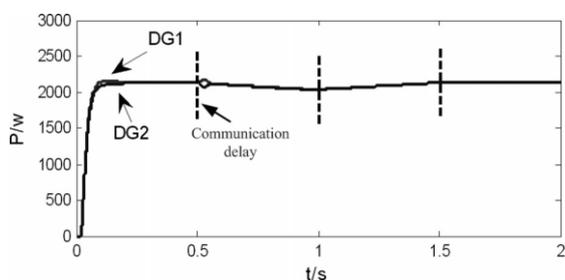


Fig.12. Output active powers of the two inverters when 0.02-s time delay occurs in synchronization signal of DG1 unit.

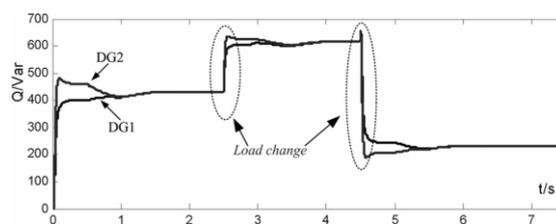


Fig.13. Reactive power sharing performance of the improved droop control (with load changing).

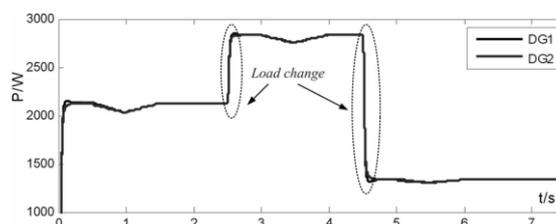


Fig.14. Active power sharing performance of the improved droop control (with load changing).

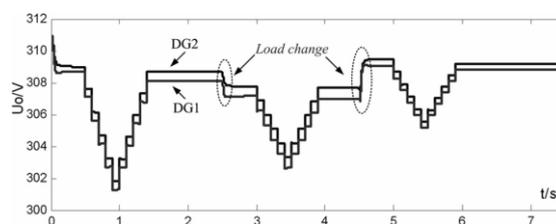


Fig.15. DG output voltage of the improved droop control (with load changing).

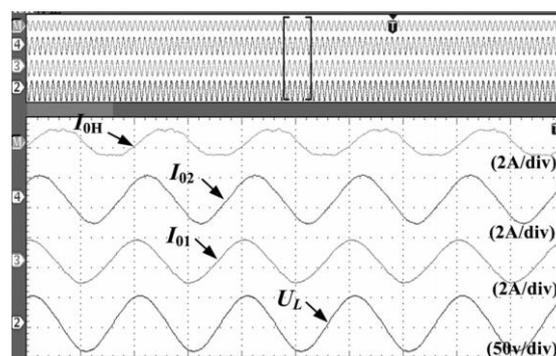


Fig.17. Steady-state experimental waveforms with the conventional droop control.

B. Experimental Results:

A microgrid prototype is built in lab as shown in Fig. 16. The microgrid consists of two micro sources based on the single phase inverter.

The parameters for output filter are the same as those in simulation. The load consists of a resistor of 16 Ω and an inductor of 3 mH. The sample frequency is 12.8 kHz. A permissible minimum time interval between two consecutive synchronization events is 0.5 s. The permissible minimum output voltage does not less than the rated voltage by 90%. Figs. 17 and 18 show the measured waveforms with the conventional and improved droop control methods, respectively.

The waveforms from top to down are circular current ($i_{0H} = i_{01} - i_{02}$), the output current of inverter 1 (i_{01}), the output current (i_{02}) of inverter 2 and PCC voltage (U_L), respectively. As can be seen from Fig. 17, there is a quite large phase difference between two output currents when the conventional droop control is applied. As a result, the circular current is pretty high and the peak value of circular current is up to 1.80 A. The main reason for it is the impedance difference of DG feeders. Compared with the circular current in Fig. 17, the circular current in Fig. 18 is very small, which indicates that the improved method is efficient in reducing the circular current mainly caused by the output reactive power difference between the inverters.

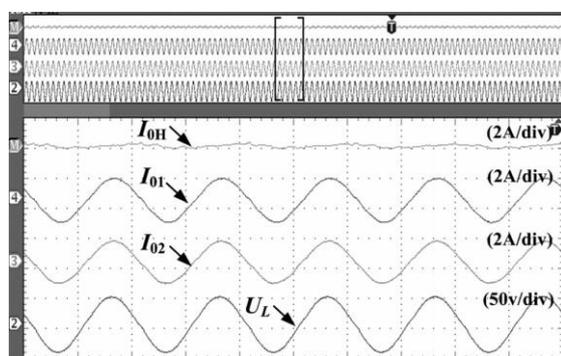


Fig18. Steady-state experimental waveforms with the improved droop control.

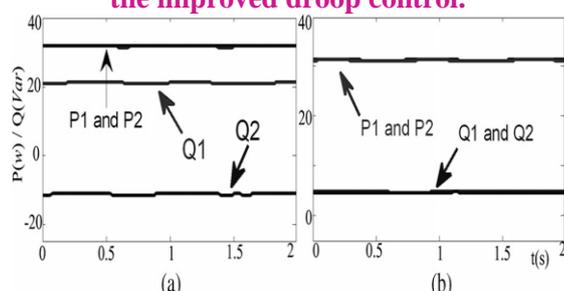


Fig.19. Steady-state active power and reactive power: (a) with the conventional droop; (b) with the improved droop control.

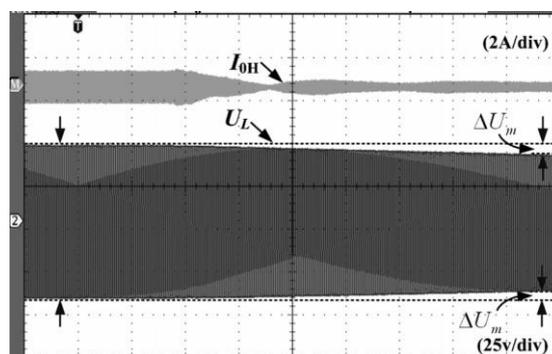


Fig 20. Circulating current and PCC voltage waveforms of DGs with only sharing error reduction operation performed.

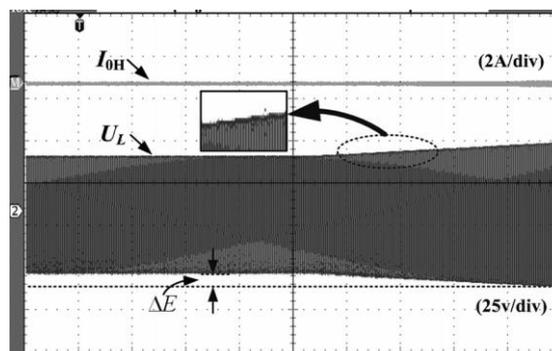


Fig.21. Circulating current and PCC voltage waveforms of DGs with only voltage recovery operation performed.

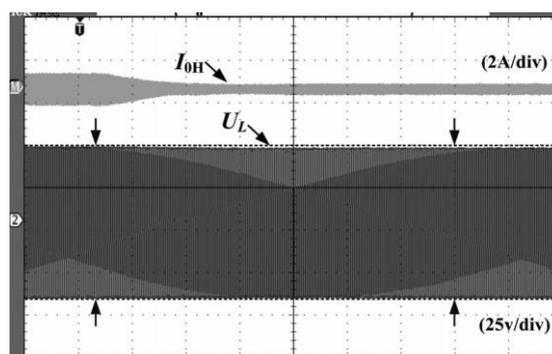


Fig 22. Circulating current and PCC voltage waveforms of DGs with the improved droop.

Fig. 19 shows the steady-state output active and reactive power of each inverter with the conventional and the improved droop control. Fig. 19(a) shows the results with the conventional droop. The steady-state output active powers of the inverters are 31.4 and 30 W, and the output reactive powers are 21.2 and –

10.4Var. When using conventional P - f droop control, no active power divergence appears since frequency is a global variable, i.e., same frequency can be measured along the microgrid; however, voltage may drop along the microgrid power lines, which produce the well-known reactive power divergence. Fig. 19(b) shows the results with the improved droop. As can be seen, the output active powers of the inverters are 30.6 and 31.1 W, and the reactive powers are 3.9 and 4.4 Var. These results indicate that the proposed improved droop control has no effect on the active power sharing performance, but makes reactive power be shared precisely.

To verify the effectiveness of the sharing error reduction operation and voltage recovery operation of the proposed method, the experiments with only one operation being continuously used are performed. As can be seen from Fig. 20, the circular current converges to a small value gradually when only the reactive power sharing error reduction operation is performed. In the meanwhile, a continuous decrease in PCC voltage could be found. Fig. 21 shows the results when only the voltage recovery operation is performed. It can be seen that the PCC voltage increases linearly during this time, and the circular current is always small and almost kept constant. Fig. 22 shows the results when the two operations are combined, i.e., the proposed method is applied. The circular current is controlled to be a small value, and the quality of the PCC voltage is guaranteed successfully.

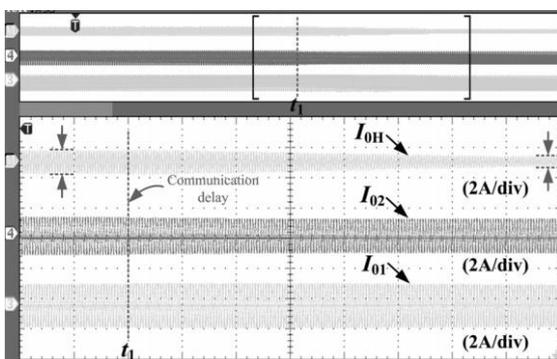


Fig.23. Output current and circulating current waveforms when 0.2-s time delay occurs in synchronization signal of DG1 unit.

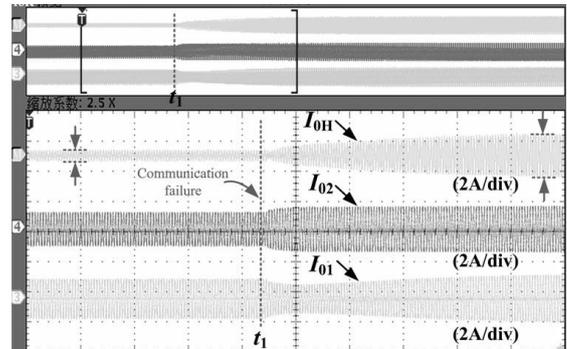


Fig.24. Output current and circulating current waveforms when the synchronization signal is lost in DG1 unit.

To test the sensitivity of the proposed method to synchronization signal, a 0.2-s delay is intentionally added to the synchronization signal received by DG1 unit every time. The associated experimental results are shown in Fig. 23. Compared to the normal case, there is no obvious difference between the two cases, and the reactive power sharing error can still reduce to a small value. Therefore, the proposed method is robust to the communication delay because all the necessary operations only need to be completed in an interval, not a critical point. Fig. 24 shows the experimental results when the synchronization signal of DG1 unit fails, which is equivalent to the time delay is infinity.

It is obvious that, before $t = t_1$, the circulating current is kept to be a small value because the improved droop control is in effect. After $t = t_1$, the sharing error reduction operation and voltage recovery operation are disabled due to the lost of the synchronization signal of DG1 unit. As a result, the peak value of the circulating current increases to about 2.8 A from a small value. In conclusion, the results in Figs. 23 and 24 indicate that the proposed method only needs a low-bandwidth requirement, and it is robust to a small time delay of communication. However, once communication fails completely, the reactive power sharing accuracy performance may be worse.

V. CONCLUSION:

In this paper, a new reactive power control for improving the reactive sharing is proposed for power electronics interfaced DG units in ac microgrids.

The proposed control strategy is realized through the following two operations: sharing error reduction operation and voltage recovery operation. The first operation changes the voltage bias of the conventional droop characteristic curve periodically, which is activated by the low-bandwidth synchronization signals. The second operation is performed to restore the output voltage to its rated value. The improved power sharing can be achieved with very simple communications among DG units. Furthermore, the plug-and-play feature of each DG unit will not be affected. Both simulations and experimental results are provided to verify the effectiveness of the proposed control strategy.

REFERENCES:

- [1] W. Deng, W. Pei, and Z. Qi, "Impact and improvement of distributed generation on voltage quality in micro-grid," in Proc. 3rd Int. Conf. Electric Utility Deregulation Restruct. Power Technol., Apr. 2008, pp. 1737–1741.
- [2] R. H. Lasseter, "Microgrids," in Proc. IEEE Power Eng. Soc. Winter Meeting, New York, NY, USA, 2002, pp. 305–308.
- [3] J. A. Peas Lopes, C. L. Moreira, and A. G. Madureira, "Defining control strategies for microgrids islanded operation," IEEE Trans. Power Syst., vol. 21, no. 2, pp. 916–924, May 2006.
- [4] Y.W. Li, D. M. Vilathgamuwa, and P. C. Loh, "Design, analysis, and realtime testing of a controller for multibus microgrid system," IEEE Trans Power Electron., vol. 19, no. 5, pp. 1195–1204, Sep. 2004.
- [5] Y. Mohamed and E. F. El-Saadany, "Adaptive decentralized droop controller to preserve power sharing stability of paralleled inverters in distributed generation microgrids," IEEE Trans. Power Electron., vol. 23, no. 6, pp. 2806–2816, Nov. 2008.
- [6] J. M. Guerrero, J. C. Vázquez, J. Matas, M. Castilla, and L. G. Vicuña, "Control strategy for flexible microgrid based on parallel lineinteractive UPS systems," IEEE Trans. Ind. Electron., vol. 56, no. 3, pp. 726–736, Mar. 2009.
- [7] J. M. Guerrero, J. C. Vázquez, J. Matas, L. Garcia de Vicuña, and M. Castilla, "Hierarchical control of droop-controlled AC and DC microgrids—A general approach towards standardization," IEEE Trans. Ind. Electron., vol. 58, no. 1, pp. 158–172, Jan. 2010.
- [8] M. C. Chandorkar, D. M. Divan, and R. Adapa, "Control of parallel connected inverters in standalone AC supply systems," IEEE Trans. Ind. Appl., vol. 29, no. 1, pp. 136–143, Jan./Feb. 1993.
- [9] J. M. Guerrero, J. Matas, L. G. de Vicuña, M. Castilla, and J. Miret, "Wireless-control strategy for parallel operation of distributed generation inverters," IEEE Trans. Ind. Electron., vol. 53, no. 5, pp. 1461–1470, Oct. 2006.
- [10] K. De Brabandere, B. Bolsens, J. Van Den Keybus, A. Woyte, J. Driesen, and R. Belmans, "A voltage and frequency droop control method for parallel inverters," IEEE Trans. Power Electron., vol. 22, no. 4, pp. 1107–1115, Jul. 2007.
- [11] J. M. Guerrero, L. G. Vicuña, J. Matas, M. Castilla, and J. Miret, "Output impedance design of parallel-connected UPS inverters with wireless loadsharing control," IEEE Trans. Ind. Electron., vol. 52, no. 4, pp. 1126–1135, Aug. 2005.
- [12] Y. W. Li and C.-N. Kao, "An accurate power control strategy for power-electronics-interfaced distributed generation units operating in a low-voltage multibus microgrid," IEEE Trans. Power Electron., vol. 24, no. 12, pp. 2977–2988, Dec. 2009.
- [13] J. M. Guerrero, J. Matas, L. Garcia de Vicuña, M. Castilla, and J. Miret, "Decentralized control for parallel operation of distributed generation inverters using resistive output impedance," IEEE Trans. Ind. Electron., vol. 54, no. 2, pp. 994–1004, Apr. 2007.
- [14] J. M. Guerrero, L. G. Vicuña, J. Matas, M. Castilla, and J. Miret, "A wireless controller to enhance dynamic performance of parallel inverters in distributed generation systems," IEEE Trans. Power Electron., vol. 19, no. 5, pp. 1205–1213, Sep. 2004.
- [15] A. Tuladhar, H. Jin, T. Unger, and K. Mauch, "Control of parallel inverters in distributed AC power systems with consideration of line impedance effect,"

IEEE Trans. Ind. Appl., vol. 36, no. 1, pp. 131–137, Jan./Feb. 2000.

[16] J. He and Y. W. Li, “An enhanced microgrid load demand sharing strategy,” IEEE Trans. Power Electron., vol. 27, no. 9, pp. 3984–3995, Sep. 2012.

[17] C. K. Sao and P. W. Lehn, “Autonomous load sharing of voltage source converters,” IEEE Trans. Power Del., vol. 20, no. 2, pp. 1009–1016, Aug. 2005.

[18] A. Moawwad, V. Khadkikar, J. L. Kirtley, “A new P-Q-V droop control method for an interline photovoltaic (I-PV) power system,” IEEE Trans. Power Del., vol. 28, no. 2, pp. 658–668, Apr. 2013.

[19] E. A. A. Coelho, P. C. Cortizo, and P. F. D. Garcia, “Small-signal stability for parallel-connected inverters in stand-alone AC supply systems,” IEEE Trans. Ind. Appl., vol. 38, no. 2, pp. 533–542, Mar./Apr. 2002.

[20] N. Pogaku, M. Prodanović, and T. C. Green, “Modeling, analysis and testing of autonomous operation of an inverter-based microgrid,” IEEE Trans. Power Electron., vol. 22, no. 2, pp. 613–625, Mar. 2007.

[21] H. Mahmood, D. Michaelson, and J. Jiang, “Accurate reactive power sharing in an islanded microgrid using adaptive virtual impedances,” IEEE Trans. Power Electron., vol. PP, no. 99, pp. 1, 1, Apr. 2014.

[22] Q.-C. Zhong, “Robust droop controller for accurate proportional load sharing among inverters operated in parallel,” IEEE Trans. Ind. Electron., vol. 60, no. 4, pp. 1281–1290, Apr. 2013.

[23] C. N. Rowe, T. J. Summers, R. E. Betz, D. J. Cornforth, and T. G. Moore, “Arctan power–frequency droop for improved microgrid stability,” IEEE Trans. Power Electron., vol. 28, no. 8, pp. 3747–3759, Aug. 2013.

[24] A. Luna, F. Blaabjerg, and P. Rodríguez, “Control of power converters in AC microgrids,” IEEE Trans. Power Electron., vol. 27, no. 11, pp. 4734–4749, Nov. 2012.

Authors:

Mr.N.Praneeth Is Working As Assistant Professor At Mother Teresa Institute Of Science And Technology, Khammam, Telangana, India. He Obtained His Bachelor Of Technology In Electrical And Electronics Engineering, From Jntuh. He Completed His Master Of Technology In Control Systems Specialization. He Is A Life Member Of Indian Society For Technical Education. He Is Having A Teaching Experience Of 6 Years. His Interested Areas Are Power Electronics And Electric Drives, Control Systems Engineering, Computer Methodology And Modeling Of Power Systems.

Miss.Kadari Navarani Is Pursuing Her Bachelor Of Technology 4th Year In Electrical And Electronics Engineering Branch At Mother Teresa Institute Of Science And Technology, Khammam, Telangana, India. Her Interested Areas Are Power Electronics, Modeling Of Power Systems And Control Systems.

Miss.Karam Umadevi Is Pursuing Her Bachelor Of Technology 4th Year In Electrical And Electronics Engineering Branch At Mother Teresa Institute Of Science And Technology, Khammam, Telangana, India. Her Interested Areas Are Power Electronics, Designing Of Electrical Machines And Power System Designing.

Miss.Katikala Venkata Aparna is Pursuing Her Bachelor Of Technology 4th Year In Electrical And Electronics Engineering Branch At Mother Teresa Institute Of Science And Technology, Khammam, Telangana, India. Her Interested Areas Are Control Systems And Neural Networks And Fuzzy Logic.