

Shunt Active Power Filter Using PV System

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Abstract:

Generally, dynamic power channels have been controlled utilizing pre-tuned controllers, for example, PI-sort or versatile, for the present and in addition for the dc-voltage circles. Dynamic power channels executed with three-stage four-leg voltage-source inverters (4L-VSI) have just been exhibited in the specialized writing, the essential commitment of this paper is a prescient control calculation outlined and actualized particularly for this application. PI controllers must be outlined in view of the equal direct model, while prescient controllers utilize the nonlinear model, which is nearer to genuine working conditions. A nitty gritty yet straightforward scientific model of the dynamic power channel, including the impact of the proportional power framework impedance, is inferred and used to outline the prescient control calculation.

Introduction

Sustainable age influences control quality because of its nonlinearity, since sunlight based age plants and wind control generators must be associated with the matrix through high-control static PWM converters. The non-uniform nature of energy age straightforwardly influences voltage control and makes voltage contortion in control frameworks. This new situation in control conveyance frameworks will require more complex remuneration methods. Albeit dynamic power channels executed with three-stage four-leg voltage-source inverters (4L-VSI) [1] have just been displayed in the specialized writing, the essential commitment of this paper is a prescient control calculation planned and actualized particularly for this application. Generally, dynamic power channels have been controlled utilizing pretuned controllers, for example, PI-sort or versatile, for the present and additionally for the dc-voltage circles. PI controllers must be composed in light of the

equal direct model, while prescient controllers utilize the nonlinear model, which is nearer to genuine working conditions. An exact model acquired utilizing prescient controllers enhances the execution of the dynamic power channel, particularly amid transient working conditions, since it can rapidly take after the present reference flag while keeping up a steady dc-voltage. Up until now, usage of prescient control in control converters have been utilized predominantly in enlistment engine drives.

On account of engine drive applications, prescient control speaks to an extremely instinctive control plot that handles multivariable attributes, improves the treatment of dead-time pay, and allows beat width modulator substitution. Be that as it may, these sorts of uses show weaknesses identified with motions and insecurity made from obscure load parameters. One favorable position of the proposed calculation is that it fits well in dynamic power channel applications, since the power converter yield parameters are notable. These yield parameters are gotten from the converter yield swell channel and the power framework proportional impedance. The converter yield swell channel is a piece of the dynamic power channel outline and the power framework impedance is acquired from surely understood standard strategies. On account of obscure framework impedance parameters, an estimation strategy can be utilized to determine a precise R– L identical impedance model [3] of the framework. This paper shows the numerical model of the 4L-VSI and the standards of operation of the proposed prescient control plot, including the plan system. The total portrayal of the chose current reference generator actualized in the dynamic power channel is additionally introduced.

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At last, the proposed dynamic power channel and the adequacy of the related control plot remuneration are shown through reenactment comes about.

System Description

The two sorts of energy age utilize air conditioning/air conditioning and dc/air conditioning static PWM converters for voltage change and battery banks for long haul vitality stockpiling. These converters perform greatest power guide following toward extricate the most extreme vitality conceivable from wind and sun. The electrical vitality utilization conduct is irregular and eccentric, and subsequently, it might be single-or three-stage, adjusted or uneven, and direct or nonlinear. A dynamic power channel is associated in parallel at the purpose of regular coupling to repay current sounds, current unbalance, and receptive power. It is formed by an electrolytic capacitor, a four-leg PWM converter [2], and a first-arrange yield swell channel, as appeared in Fig. 1.

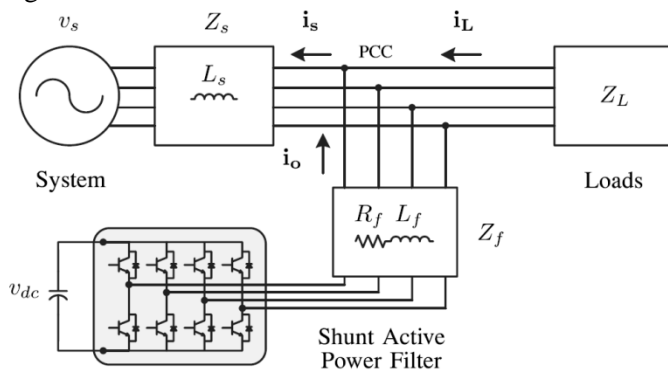


Figure 1: Three-phase equivalent circuit of the proposed shunt active power filter.

This circuit considers the power framework comparable impedance Z_s , the converter yield swell channel impedance Z_f , and the heap impedance Z_L . The four-leg PWM converter topology is appeared in Fig. 2. The voltage in any leg x of the converter, measured from the nonpartisan point (n), can be communicated as far as exchanging states, as takes after:

$$V_{xn} = S_x - S_n V_{dc}, \quad x = u, v, w, n.$$

The numerical model of the channel got from the proportional circuit appeared in Fig. 3 is

$$V_o = V_{xn} - R_{eq} i_o - L_{eq} (di_o/dt)$$

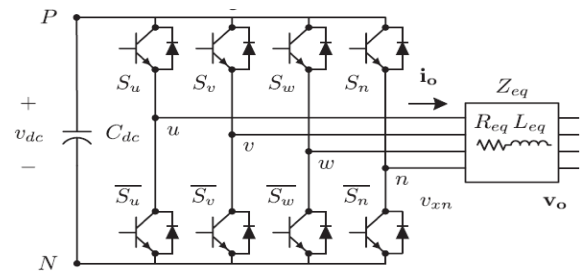


Figure 2: Two-level four-leg PWM-VSI topology

Where R_{eq} and L_{eq} are the 4L-VSI yield parameters communicated as Thevenin impedances at the converter yield terminals Z_{eq} . Hence, the Thevenin equal impedance is controlled by an arrangement association of the swell channel impedance Z_{fand} a parallel game plan between the framework comparable impedance Z_s and the heap impedance Z_L [4].

For this model, it is expected that $Z_L > Z_s$, that the resistive piece of the framework's identical impedance is ignored, and that the arrangement reactance is in the scope of 3–7% p.u., which is a satisfactory estimation of the genuine framework. At last, $R_{eq} = R_{fand}$ and $L_{eq} = L_s + L_f$.

Control Strategy

Computerized prescient current control

The square outline of the proposed advanced prescient current control conspire is appeared in Fig. 3. This control conspire is fundamentally a streamlining calculation and, consequently, it must be executed in a microchip. Subsequently, the examination must be created utilizing discrete arithmetic so as to consider extra limitations, for example, time deferrals and approximations. The principle normal for prescient control is the utilization of the framework model to anticipate the future conduct of the factors to be controlled. The controller utilizes this data to choose the ideal exchanging state that will be connected to the power converter, as indicated by predefined streamlining criteria. The prescient control calculation is anything but difficult to execute and to comprehend, and it can be actualized with three principle hinders, as appeared in Fig. 3

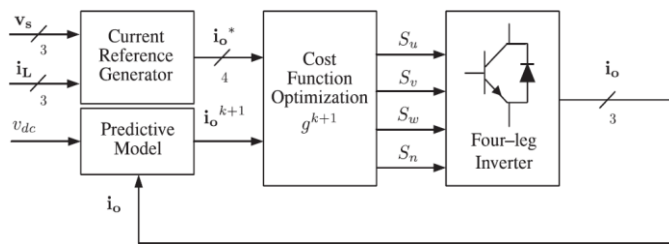


Figure 3: Proposed predictive digital current control block diagram

Current Reference Generator

This unit is intended to produce the required current reference that is utilized to remunerate the unfortunate load current parts. For this situation, the framework voltages, the heap streams, and the dc-voltage converter are measured, while the unbiased yield present and impartial load current are created specifically from these signs.

The converter display is utilized to foresee the yield converter current. Since the controller works in discrete time, both the controller and the framework demonstrate must be spoken to in a discrete time area. The discrete time demonstrate comprises of a recursive network condition that speaks to this forecast framework. This implies for a given testing time T_s , knowing the converter exchanging states and control factors at moment kT_s , it is conceivable to anticipate the following states at any moment $[k + 1]T_s$ [5]. Due to the primary request nature of the state conditions that portray the model, an adequately exact first-arrange estimation of the subsidiary is considered in this paper.

Keeping in mind the end goal to foresee the yield current i_o at the moment $(k + 1)$, the information voltage esteem V_o and the converter yield voltage V_{xN} , are required. The calculation computes each of the 16 esteems related with the conceivable mixes that the state factors can accomplish.

A dq-based current reference generator conspire is utilized to get the dynamic power channel current reference signals. This plan shows a quick and precise flag following ability. This trademark maintains a

strategic distance from voltage changes that disintegrate the present reference flag influencing pay execution. The present reference signals are acquired from the relating load streams as appeared in Fig. 4. This module computes the reference flag streams required by the converter to repay receptive power, current symphonious and current lopsidedness.

Where the estimation of THD(L) incorporates the most extreme compensable symphonious present, characterized as twofold the testing recurrence fs. The recurrence of the most extreme current consonant segment that can be remunerated is equivalent to one portion of the converter exchanging recurrence.

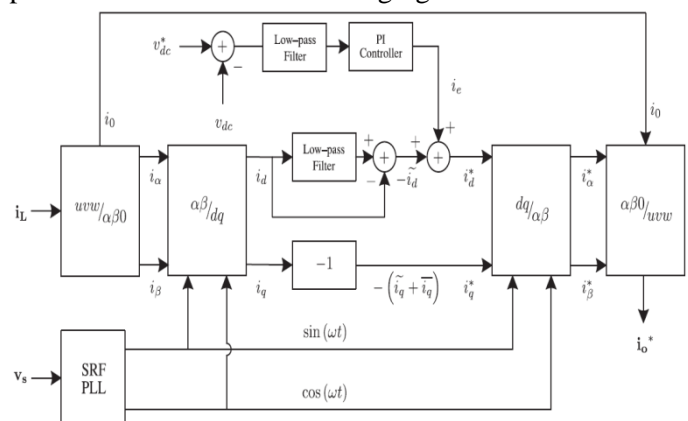


Figure 4: dq-based current reference generator block diagram

Where the estimation of THD(L) incorporates the greatest compensable symphonious present, characterized as twofold the inspecting recurrence fs. The recurrence of the most extreme current symphonious part that can be repaid is equivalent to one portion of the converter exchanging recurrence.

The dq-based plan works in a turning reference outline; in this manner, the deliberate streams must be increased by the $\sin(\omega t)$ and $\cos(\omega t)$ signals. By utilizing dq-change, the d current part is synchronized with the relating stage to-nonpartisan framework voltage, and the q current segment is stage moved by 90° . The $\sin(\omega t)$ and $\cos(\omega t)$ synchronized reference signals are gotten from a synchronous reference outline (SRF) PLL. The

SRF-PLL produces an unadulterated sinusoidal waveform notwithstanding when the framework voltage is extremely misshaped. Following mistakes are disposed of, since SRF-PLLs are intended to maintain a strategic distance from stage voltage unbalancing, harmonics (i.e., under 5% and 3% in fifth and seventh, individually), and balance caused by the nonlinear load conditions and estimation blunders.

Permit pass channel extricates the dc segment of the stage streams i_d to create the symphonious reference parts $-i_d$. The responsive reference segments of the stage streams are acquired by stage moving the relating air conditioning and dc parts of i_q by 180° . So as to keep the dc-voltage consistent, the abundance of the converter reference current must be changed by including a dynamic power reference flag i_e with the d-segment, as will be clarified in Section IV-A. The subsequent signs i^*d and i^*q are changed back to a three-stage framework by applying the reverse Park and Clark change. The cutoff recurrence of the LPF [2] utilized as a part of this paper is 20 Hz.

The present that courses through the unbiased of the heap is remunerated by infusing the same prompt esteem acquired from the stage streams, stage moved by 180° , as appeared next

$$i^*_{on} = -(i_{Lu} + i_{Lv} + i_{Lw})$$

One of the significant favorable circumstances of the dq-based current reference generator conspire is that it permits the execution of a direct controller in the dc-voltage control circle. The sufficiency of this symphonious relies upon the percent of uneven load current (communicated as the connection between the negative grouping current $i_{L,2}$ and the positive arrangement current $i_{L,1}$). The second-arrange consonant can't be expelled from i_d and i_q , and subsequently produces a third symphonious in the reference current when it is changed over back to abc outline. Since the heap current does not have a third consonant, the one produced by the dynamic power channel streams to the power framework.

DC-Voltage Control

The dc-voltage converter is controlled with a customary PI controller. This is an imperative issue in the assessment, since the cost work is composed utilizing just current references, with a specific end goal to maintain a strategic distance from the utilization of weighting factors. By and large, these weighting factors are acquired tentatively, and they are not very much characterized when diverse working conditions are required.

The dc-voltage stays steady (with a base estimation of $\sqrt{6}V_s(\text{rms})$) until the point that the dynamic power consumed by the converter reductions to a level where it can't adjust for its misfortunes. The dynamic power consumed by the converter is controlled by altering the sufficiency of the dynamic power reference flag i_e , which is in stage with each stage voltage. In the square outline appeared in Fig. 5, the dc-voltage v_{dc} is measured and afterward contrasted and a consistent reference esteem v^*_{dc} . Fig. 5 demonstrates that the yield of the PI controller is sustained to the dc-voltage exchange work G_s , which is spoken to by first-arrange framework.

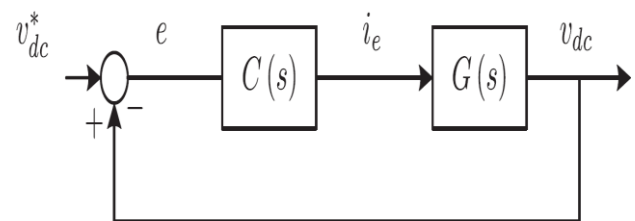


Figure 5: DC-voltage control block diagram.

Simulation Results

This section shows an examination and reproduction of a PV interactive Shunt Active Power Filter topology that achieves simultaneously consonant current damping and receptive power compensation. For the Shunt Active Filter reference current computation, we utilized the Synchronous Reference Frame Theory (SRF) [3]. What's more, to gate flag age we apply the carrier-based PWM tweak.

Figure 16 demonstrates the proposed framework: a three stage source is connected to a nonlinear load. A DC/DC

converter jars be used to change the estimation of the yield voltage of PV energy source to the voltage estimation of the dc-side capacitor of the Shunt Active Filter.

The DC/DC converter is additionally in charge of following the greatest power point (MPPT) of the PV modules to fully use the PV control. Likewise, the inverter is constantly utilized to act as a dynamic power channel to remunerate the nonlinear load harmonics and responsive power.

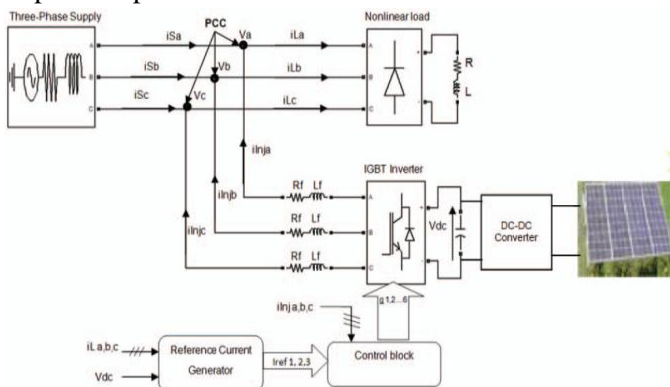


Fig 6: Configurations of photovoltaic interactive Shunt Active Filter system

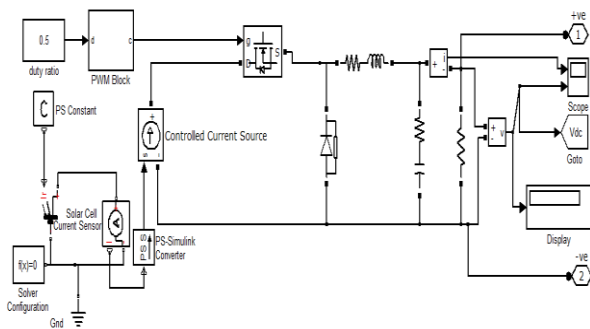


Fig 7: Simulink design of PV system

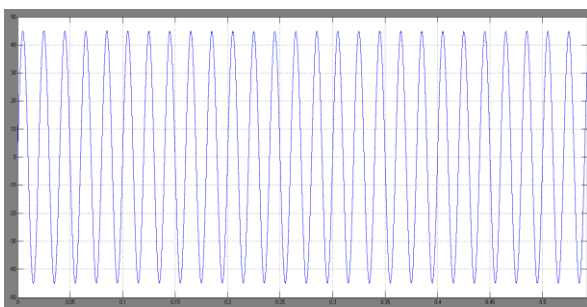


Fig 8: Phase to neutral source voltage

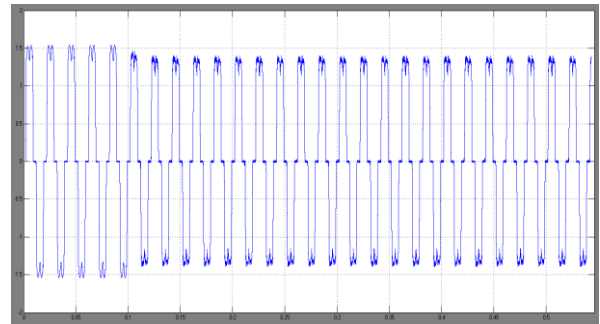


Fig 9: Load Current

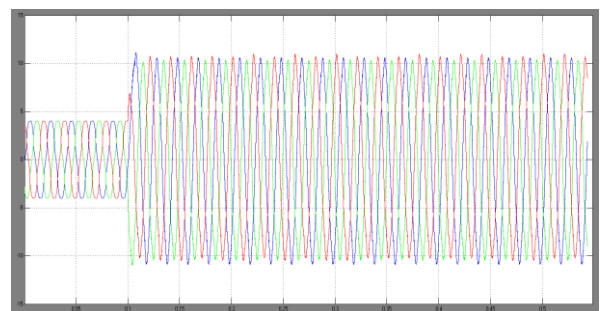


Fig 10: System currents

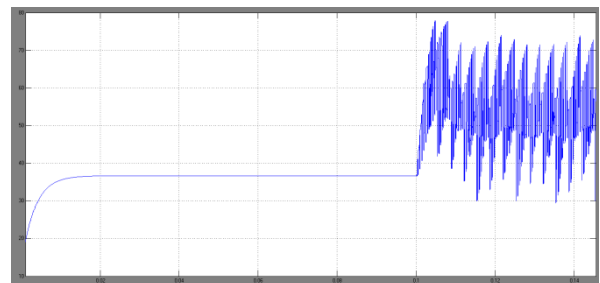


Fig 11: PV DC voltage

CONCLUSION

Enhanced dynamic current harmonics and a responsive power pay plot for control dissemination frameworks with age from sustainable sources has been proposed to enhance the present nature of the conveyance framework. Points of interest of the proposed plot are identified with its effortlessness, displaying, and execution. Reproduced comes about have demonstrated that the proposed prescient control calculation is a decent other option to traditional straight control strategies. The prescient current control calculation is a steady and powerful arrangement. Recreated comes about have demonstrated the pay adequacy of the proposed dynamic power channel with PV framework.

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