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An Efficient Image Transmission Technique Using Wireless Multimedia Sensor Network

A Akshitha
MTech Student
Aurora Technological and Research Institute

K Hari Krishna, MTech
Assistant Professor
Aurora Technological and Research Institute

Abstract— ***The concept of obstacle to human action pictures over wireless detector networks has been the dearth of appropriate process architecture and communication methods to take care of the large volume of knowledge. High packet error rates and therefore the want for retransmission build it inefficient in terms of energy and bandwidth. This paper presents novel design and protocol for energy economical image process and communication over wireless detector networks. Sensible results show the effectiveness of these approaches to create image communication over wireless sensor networks possible, reliable and economical. This application note explains a way to connect a Kinetic MCU to CMOS device through GPIO to receive image knowledge in memory. The appliance will be enforced on Kinetic MCUs and CMOS devices with CSI (CMOS sensor interface) gutlessly, with none external hardware. During this case the implementation was done by mistreatment Tower analysis board for Kinetic K60 and CMOS image device. The compatibility of Kinetic MCUs permits this application to be enforced on different sub-families of Kinetic apart from K60. The actual fact all Kinetic K series shares a similar modules (IP blocks) permits easy use of the code across the whole K sub-families. Examples of target applications for this application note area unit security systems and different shopper merchandise that specialize in low cost and low power***

Keywords: ***CMOS, Image device, Wireless Multimedia Sensor Network, GPIO, MCU.***

INTRODUCTION:

The represents the design of the projected WMSN processing system. The network processor performs some standard operations further as custom directions to support the operations of the wireless transceiver. It operates at a low clock frequency to stay the facility consumption low. The image process block runs at a high frequency to method images at a high speed. By default, it's in inactive mode (sleep mode with suppressed clock source), and might be quickly set into the active mode by the network processor whenever AN object extraction task must be performed. In WMSN applications, the camera atom typically incorporates a fastened frame of read. during this case, to discover moving (updated) objects, background subtraction may be a ordinarily used approach The basic idea of this can be to discover the objects from the difference between the present frame and also the background image. The background image represents a static scene of the camera read with none moving objects. AN algorithmic program should be applied to stay the background image frequently updated to adapt to the changes within the camera read. For background subtraction, the Running mathematician Average seems to own the quickest process speed and lowest memory necessities it's additional optimised for ARM implementation and is incorporated into the projected WMSN system

DESIGN IMPLEMENTATION

OVERVIEW OF THE SYSTEM

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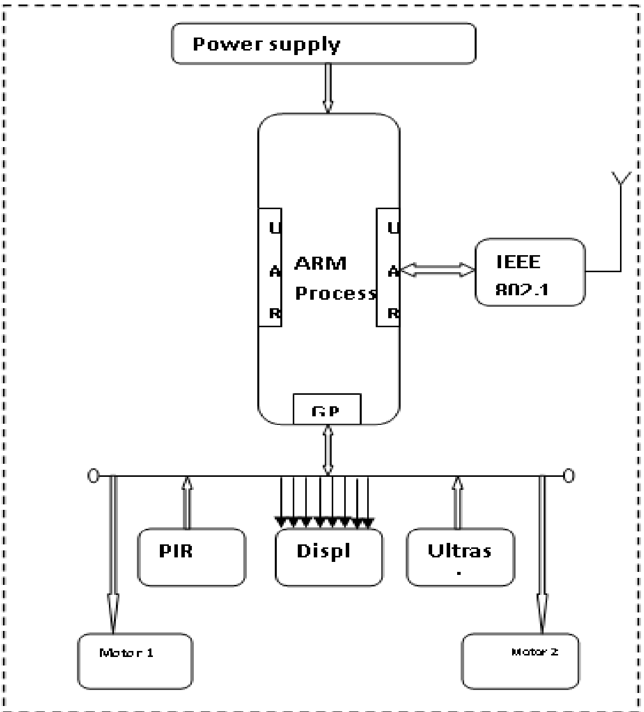

A)Back ground subtraction algorithm for image compression

Background subtraction, also known as Foreground Detection, is a technique in the fields of image processing and computer vision wherein an image's foreground is extracted for further processing (object recognition etc.). Generally an image's regions of interest are objects (humans, cars, text etc.) in its foreground. After the stage of image preprocessing (which may include image denoising etc.) object localisation is required which may make use of this technique. Background subtraction is a widely used approach for detecting moving objects in videos from static cameras. The rationale in the approach is that of detecting the moving objects from the difference between the current frame and a reference frame, often called “background image”, or “background model”.[1] Background subtraction is mostly done if the image in question is a part of a video stream.E

B)image regnition

Image recognition is composed of two parts: classification and validation. The classification can be done somewhat easily by statistics of dimensions and pattern features of each type of image. On the other hand, validation is very difficult because we cannot obtain counterfeits that might appear in future, while we can collect plenty of genuine images. Moreover, statistics for a two-class (genuine and counterfeit banknotes) problem has less power because counterfeits could not actually be collected. Our approach is therefore to carefully select observation points at which a physical feature has a small deviation amongst genuine banknotes and looks difficult to imitate

C) Wireless communication

The XBee/XBee-PRO RF Modules are designed to operate within the ZigBee protocol and support the unique needs of low-cost, low-power wireless sensor networks. The modules require minimal power and provide reliable delivery of data between remote devices. The modules operate within the ISM 2.4 GHz frequency band and are compatible with the following:

- XBee RS-232 Adapter
- XBee RS-232 PH (Power Harvester) Adapter
- XBee RS-485 Adapter
- XBee Analog I/O Adapter
- XBee Digital I/O Adapter
- XBee Sensor Adapter
- XBee USB Adapter
- XStick
- Connect Port X Gateways
- XBee Wall Router.

The XBee/XBee-PRO ZB firmware release can be installed on XBee modules. This firmware is compatible with the ZigBee 2007 specification, while the ZNet 2.5 firmware is based on Ember's proprietary "designed for ZigBee" mesh stack (EmberZNet 2.5). ZB and ZNet 2.5 firmware are similar in nature, but not over-the-air compatible. Devices running ZNet 2.5 firmware cannot talk to devices running the ZB firmware.

Key Features:

➢ **High Performance, Low Cost**

- Indoor/Urban: up to 300’ (100 m)
- Outdoor line-of-sight: up to 1 mile (1.6 km)
- Transmit Power Output: 100 mW (20 dBm) EIRP
- Receiver Sensitivity: -102 dBmRF Data rate: 250,000 bps.

➢ **Advanced Networking & Security**

- Retries and Acknowledgements
- DSSS (Direct Sequence Spread Spectrum)
- Each direct sequence channel has over 65,000 unique network addresses available
- Point-to-point topology
- point-to-multipoint topology
- Self-routing, self-healing and fault-tolerant
- mesh networking

➢ **Low Power**

- TX Current: 295 mA (@3.3 V)
- RX Current: 45 mA (@3.3 V)
- Power-down Current: < 1 µA @ 25oC

➢ **Easy-to-Use**

- No configuration necessary for out-of box
- RF communications
- AT and API Command Modes for configuring module parameters
- Small form factor
- Extensive command set
- Free X-CTU Software (Testing and configuration software)

Mounting Considerations

The XBee modules were designed to mount into a receptacle (socket) and therefore do not require any soldering when mounting it to a board. The XBee-PRO Development Kits contain RS- 232 and USB interface boards which use two 20-pin receptacles to receive modules. Figure 4.7.2 XBee-PRO Module Mounting to an RS-232 Interface Board.

The receptacles used on Digi development boards are manufactured by Century Interconnect. Several other manufacturers provide comparable mounting solutions; however, Digi currently uses the following receptacles:

- Through-hole single-row receptacles - Samtec P/N: MMS-110-01-L-SV (or equivalent)
- Surface-mount double-row receptacles Century Interconnect P/N: CPRMSL20-D-0-1
- Surface-mount single-row receptacles - Samtec P/N: SMM-110-02-SM-S

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Pin Assignment of Zigbee Module:

Table : Pin Assignment of Zigbee Module

Pin #	Name	Direction	Description
1	VCC	-	Power supply
2	DOUT	Output	UART Data Out
3	DIN / CONFIG	Input	UART Data In
4	DIO12	Either	Digital I/O 12
5	RESET	Input	Module Reset (reset pulse must be at least 200 ns)
6	PWM0 / RSSI / DIO10	Either	PWM Output 0 / RX Signal Strength Indicator / Digital IO
7	PWM / DIO11	Either	Digital I/O 11
8	[reserved]	-	Do not connect
9	DTR / SLEEP_RQ/ DIO8	Either	Pin Sleep Control Line or Digital IO 8
10	GND	-	Ground
11	DIO4	Either	Digital I/O 4
12	CTS / DIO7	Either	Clear-to-Send Flow Control or Digital I/O 7
13	ON / SLEEP / DIO9	Output	Module Status Indicator or Digital I/O 9
14	VREF	Input	Not used on this module. For compatibility with other XBee modules, we recommend connecting this pin to a voltage reference if Analog sampling is desired. Otherwise, connect to GND.
15	Associate / DIO5	Either	Associated Indicator, Digital I/O 5
16	RTS / DIO6	Either	Request-to-Send Flow Control, Digital I/O 6
17	AD3 / DIO3	Either	Analog Input 3 or Digital I/O 3
18	AD2 / DIO2	Either	Analog Input 2 or Digital I/O 2
19	AD1 / DIO1	Either	Analog Input 1 or Digital I/O 1
20	AD0 / DIO0 / Commissioning Button	Either	Analog Input 0, Digital IO 0, or Commissioning Button

Design Notes:

- Minimum connections: VCC, GND, DOUT & DIN
- Minimum connections to support serial firmware upgrades: VCC, GND, DIN, DOUT, RTS & DTR
- Signal Direction is specified with respect to the module
- Module includes a 30k Ohm resistor attached to RESET
- Several of the input pull-ups can be configured using the PR command
- Unused pins should be left disconnected
- Pin 20 can be connected to a push button (pin grounded when closed) to support the commissioning push button functionality. See "Commissioning Pushbutton and Associate LED" for details.

Table :Electrical Characteristics of Zigbee Module

Electrical Characteristics

Table 1-03. DC Characteristics of the XBee-PRO (VCC = 3.0 - 3.4 VDC).

Symbol	Parameter	Condition	Min	Typical	Max	Units
VIL	Input Low Voltage	All Digital Inputs	-	-	0.2 * VCC	V
V_{IH}	Input High Voltage	All Digital Inputs	0.8 * VCC	-	-	V
V_{OL}	Output Low Voltage	I_{OL} = 2 mA, VCC >= 2.7 V	-	-	0.18*VCC	V
V_{OH}	Output High Voltage	I_{OH} = -2 mA, VCC >= 2.7 V	0.82*VCC	-	-	V
I_{IN}	Input Leakage Current	V_{IN} = VCC or GND, all inputs, per pin	-	-	0.5uA	uA

RF Module Operation

Serial Communication:

The XBee OEM RF Modules interface to a host device through a logic-level asynchronous serial port. Through its serial port, the module can communicate with any logic and voltage compatible UART; or through a level translator to any serial device.

UART Data Flow

Devices that have a UART interface can connect directly to the pins of the RF module as shown in the figure below.

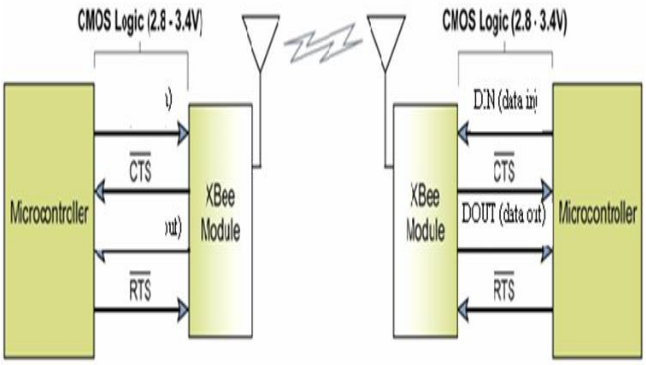

Figure: Zigbee UART Dataflow

Data enters the module UART through the DIN (pin 3) as an asynchronous serial signal. The signal should idle high when no data is being transmitted. Each data byte consists of a start bit (low), 8 data bits (least significant bit first) and a stop bit (high). The following figure illustrates the serial bit pattern of data passing through the module.

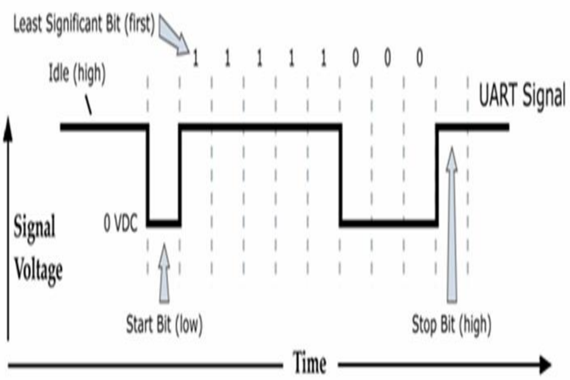

Figure Serial Data Format

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The module UART performs tasks, such as timing and parity checking, that are needed for data communications. Serial communications depend on the two UARTs to be configured with compatible settings (baud rate, parity, start bits, stop bits, data bits).

Serial Buffers

The XBee modules maintain small buffers to collect received serial and RF data, which is illustrated in the figure below. The serial receive buffer collects incoming serial characters and holds them until they can be processed. The serial transmit buffer collects data that is received via the RF link that will be transmitted out the UART.

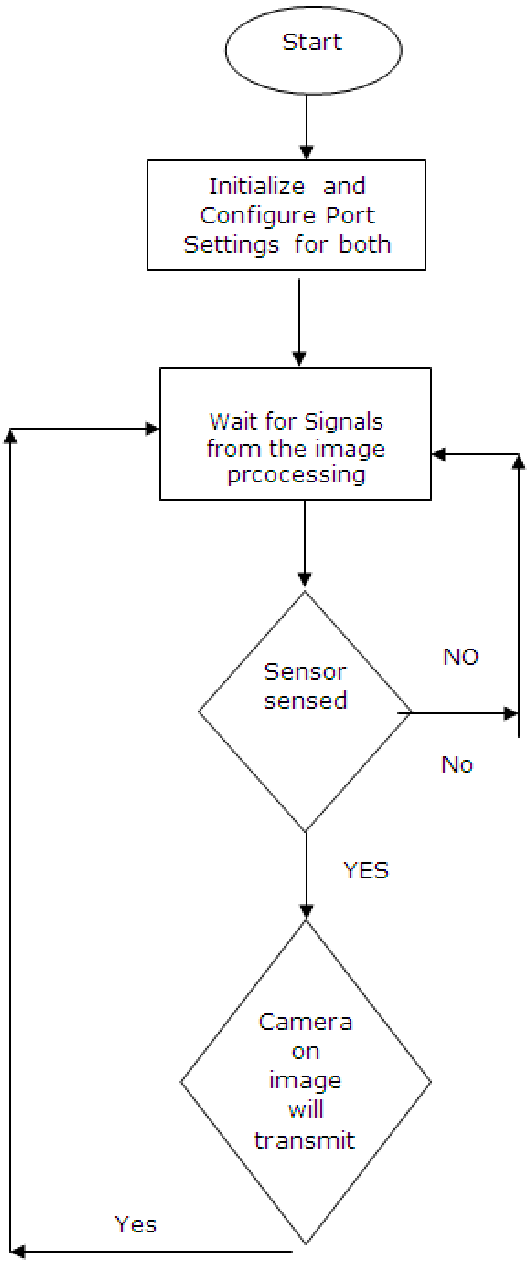
E)UART communication:

We are using serial communication technique to make a communication between PC and Processor.The rate of data transfer in serial data communication is stated in bps (bits per second). Another widely used terminology for bps is baud rate. However, the baud and bps rates are not necessarily equal. This is due to the fact that baud rate is the modem terminology and is defined as the number of signal changes per second. In modems, there are occasions when a single change of signal transfers several bits of data. As far as the conductor wire is concerned, the baud rate and bps are the same.

In RS232, a 1 is represented by -3 to -25V, while a 0 bit is +3 to +25V, making -3 to +3 undefined. For this reason, to connect any RS232 to a microcontroller system we must use voltage converters such as MAX232 to convert the TTL logic levels to the RS232 voltage level, and vice versa. MAX232 IC chips are commonly referred to as line drivers.

The ARM transfers and receives data serially at many baud rates. The baud rate in the ARM is programmable. This is done with the help of timer 1.The relationship between the crystal frequency and the baud rate is discussed here.

The ARM divides the crystal frequency by 12 to get the machine cycle frequency. In the case of XTAL = 11.0592 MHz, the machine cycle frequency is 921.6 kHz (11.0592 MHz / 12 = 921.6 kHz). The ARM's serial communication UART circuitry divides the machine cycle frequency of 921.6 kHz by 32 once more before timer 1 to set the baud rate uses it. Therefore, 921.6 kHz divided by 32 gives 28,800 Hz.Using this interface we are communicating between the PC and the Processor:



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CONCLUSION

This wireless image transfer unit in robot will be ready to capture any human who enters into robot survelliance region. this presence of human can be determined by the thermal heat radiation from the object and this interrupt will wake up the camera. in order to cover the entire room , the robot module can able to turn in various angle with manual and automatic control. under the absence of human the processor will keep the camera in the sleep mode. the technique is followed in the project to reduce the energy consumption in image transmission. these methods improves the efficiency of the robot surveillance as well as energy saving. this project uses the matlab platform for the image visualization

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