

A DC Current Flow Controller for Meshed Modular Multilevel Converter Multiterminal HVDC Grids

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Abstract:

This paper proposes the design of a novel DC current flow controller (CFC) and evaluates the control performance of balancing and regulating the DC branch currents using the DC CFC in a meshed multiterminal HVDC (MTDC) grid. The DC CFC consists of two identical full bridge DC-DC converters with the capacitors of the two converters being connected in parallel. The scalability of the DC CFC is easily achievable due to the identical bridge converter topology; the cost of this DC CFC is also relatively low due to its simple physical structure and low voltage ratings. The control performance of the DC CFC is tested on a meshed 3-terminal (3-T) HVDC grid, which is based on modular multilevel converters (MMC). The DC branch current control in the meshed MTDC grid is achieved using the proposed control strategy of the DC CFC, and is verified through case studies on the real-time digital simulator (RTDS).

Index Terms:

Capacitor voltage control, DC branch current control, DC current flow controller, HVDC transmission, meshed multi-terminal HVDC grid, modular multilevel converter.

I. INTRODUCTION:

The HVDC transmission technology is recognized as a advantageous approach for worldwide long-distance bulk-power transmissions [1], [2], with several HVDC applications currently in use for MTDC technologies [3]-[6]. China, at present, has two multiterminal HVDC grids in operation [5], [6]. Due to its superiority in more efficiently utilizing and integrating renewable energy located in remote areas,

MTDC technology has become more attractive in recent years compared to traditional point-to-point HVDCs [7]-[10]. The MTDC system can also be reconfigured into different topologies under faults, particularly after a faulted line is isolated, in order to increase the continuity and reliability of the power supply [10]. Regarding the MTDC technology. Basically, the VSC based technology has more advantages over the LCC based technology [11], [12]. This is because the vector control of the VSC converters, which realizes the independent control of active and reactive power. Hence, for the regulation of power, the VSC based MTDC system is considered more flexible, particularly in instances where the power flow reversal can be easily achieved by reversing the direction of DC currents rather than the reversal of DC voltages. Based on these characteristics, MTDC applications are being increasingly used in HVDC transmission.

Radial interconnections of DC grids, in particular, are being predominantly considered [13], [14] due to their simple configuration and control strategies for regulating power distribution. In a radial topology, there is only one path between two electric nodes, so the power is fully regulated. Although the radial topology is simple and easy to realize, the meshed topology of DC grids, similar to that of AC grids, is considered as more favorable for real power applications [13], [14]. This is because the meshed topology increases the redundancy of power transmission, which contributes to the enhancement of the reliability of the power system transmission [15], [16]. In a meshed DC grid, the total power exchanged at the converter DC side can be fully controlled;

However, the DC current of each branch, depending on the voltage difference of two DC terminals and the resistance of the DC branch, may not be controllable. If there is no additional control strategy to balance the branch currents, the distribution of branch currents will be determined by Kirchhoff's laws. There is a potential risk that one or more branches of a DC grid may become overloaded, while other branches may be underutilized, since more currents will inherently be delivered to the branch of lower resistance. Therefore, the complexity of the meshed DC grid leads to the potential problems, which are the main concerns of this paper. There are several control strategies for meshed MTDC grids that have been proposed in the literature. In [17], [18], different droop controllers have been investigated for MTDC grids including meshed topologies. It was found that while the active power of each terminal could be coordinated to a certain extent, the distribution of the DC current on each branch was incapable of being accurately controlled in the meshed grid.

A power flow control device for a meshed DC grid has been designed and demonstrated in [13]. In this work, the DC branch current is well controlled by switching on and off the variable resistance of the device. However, the power loss due to switch-in resistance is an undesirable outcome. Reference [19] has presented a power flow control device that provides a detailed system configuration and basic control logic for a meshed DC. However, the control strategy in this device has not been comprehensively analyzed nor a detailed control strategy proposed. A conceptual DC control flow controller (CFC) has been proposed in [14] in which standard full-bridge DC-DC converters with low voltage ratings are used to design the controller. However, only a basic conceptualization of this control device is introduced, and the performance of the branch current control capability is not fully illustrated, while the operating principle and detailed control approach of the device are also not thoroughly investigated.

NOMENCLATURE

$n-T$	n terminal ($n = 1, \dots, n$).
T_n	Terminal n ($n = 1, \dots, n$).
SM	Submodule.
CFC	Current flow controller.
IGBT	Insulated-gate bipolar transistor.
KCL	Kirchhoff's current law.
LCC	Line commutated converter.
VSC	Voltage sourced converter.
MMC	Modular multilevel converter.
PWM	Pulse-width modulation.
RTDS	Real-time digital simulator.

In this paper, the design of a DC CFC, particularly its detailed control strategy of branch currents, is proposed for a meshed 3-T MMC HVDC grid. The DC CFC is established based on the concept in [14]. The objective of the DC CFC is to control the DC branch currents by transferring the additional power from the overloaded branch to the underutilized branch and to realize this objective with relatively low power losses. The DC branch currents can be regulated at a certain range using the proposed control. The validity of the proposed control strategy of the DC CFC is verified through case studies on the RTDS.

The rest of this paper is organized as follows. Section II presents the meshed 3-T MMC-HVDC system with the system configuration, the control strategy of the MMC at each terminal and an equivalent DC power flow analysis. Section III introduces the meshed MTDC system with the installation of a DC CFC along with the structure of the DC CFC. Section IV explains the proposed control strategy of the DC CFC and features with the proposed control. Case studies and analysis are presented in Section V, followed by conclusions drawn in Section VI.

II. MESHED 3-T MMC-HVDC SYSTEM:

A. System Configuration:

A single-line schematic diagram of the investigated meshed 3-T MMC-HVDC system is shown in Fig. 1 (a). The three terminals are T1, T2, and T3.

The MTDC system has three (3) identical MMCs. Each MMC, as shown in Fig. 1(b), comprises six (6)

converter arms and each arm has n series half-bridge SMs and one series inductor L_{arm} per converter arm. Each SM includes an energy storage capacitor C and two switching valves (81,82). Only one switch is switched on during normal operation. Therefore, the output voltage V_{SM} of each SM is either equal to the capacitor voltage V_c when the upper switch 81 is switched on, or equal to zero when the lower switch 82 is switched on. The MMC modeled in this paper has 6 SMs per converter arm. The MMC DC side is connected to the DC cable, while the MMC AC side is connected to an AC grid at each terminal through a series inductor and resistor as well as a three-phase transformer. For the DC system, it is a ± 50 kV meshed 3-T DC grid. The DC cables are modeled as lumped resistors and the resistance of the DC cable between each terminal, as shown in Table I, is different. For the AC systems of the 3 terminals, their configurations and parameters are identical. Complete system parameters are shown in Table-I

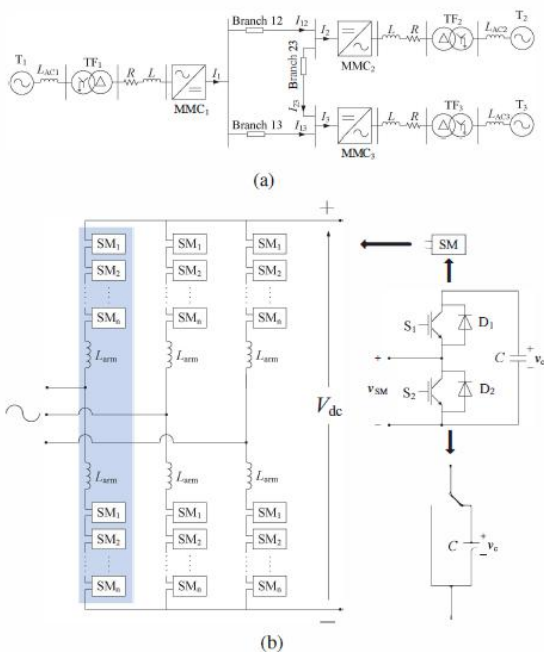


Fig.1. Configuration of a meshed 3-T MMC-HVDC grid. (a) System configuration. (b) MMC configuration.

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B. Control Strategies of the MMCs

The MMCs are considered as VSC type converters [20]- [22]. The converter level control of the model in this paper applies the classic vector control strategy. Both MMC1 and MMC2 apply constant active power control. For the sake of simplification of the system analysis, some assumptions are made: 1) the losses of the converters are neglected; 2) the AC system voltage is constant due to the connection to the AC utility grid; 3) the MMC DC side current is regulated to be constant via the active power control. The total current

TABLE I
PARAMETERS OF THE 3-T MESHED MMC-HVDC SYSTEM IN FIG. 1

Parameter	Value	Parameter	Value
Nominal AC source voltage	138 kV (L-N)	Nominal DC voltage	± 50 kV
L_{AC}	150 mH	MMCs rated capacity	150 MVA
Nominal AC frequency	50 Hz	L_{arm}	3 mH
Transformer voltage ratio	138 kV/30 kV (Y/ Δ)	Number of SMs per arm	6
Transformer rating	150 MVA	Submodule capacitor	2,500 μ F
Transformer leakage inductance	5%	Branch 12 cable resistance R_{12}	1 Ω
R	0.03 Ω	Branch 13 cable resistance R_{13}	2 Ω
L	1 mH	Branch 23 cable resistance R_{23}	1 Ω

imported from T 1 to the DC grid is kept at 1 kA, while the total current exported from the DC grid to T 2 is kept at 0.4 kA. MMC3 is controlled to maintain the DC voltage of T 3 at ± 50 kV. The reactive power is controlled to be at 0 by all three MMCs. The capacitor voltage balancing strategy of the MMC employs the conventional sorting method [12]. That is, depending on the arm current direction, the capacitor with a relative lower voltage is charged first, while the capacitor with a relative higher voltage also discharges first.

The DC branch currents I_{12} , I_{13} and I_{23} , which flow via Branch 12, 13, and 23, are not controlled. Thus the distribution of the branch currents largely depends on the cable resistance of each branch. Although most DC cables are constructed of the same material, the lengths of the DC cables between terminals are different and environmental conditions, such as temperature may lead to differences in cable resistance. Therefore, in principle, the cable resistance of each branch is considered to have different values. If there is no additional strategy to control the DC branch current, the distribution of branch currents, for instance, I_{12} , I_{13} and I_{23} , of the DC grid, are uncontrollable. One or more branches, therefore, may become overloaded, while the other is insufficiently utilized. The principle of electric power transmission is to achieve a maximum utilization of the transmission line/cable within its transfer capability [15]. Therefore, the control of the branch currents is inevitable and necessitates additional controllers.

Since the branch currents of a meshed DC grid are determined by DC cable resistances and DC voltage differences between two terminals, two approaches can be used to control the branch current. One is to change the cable resistance by adding an additional resistor on the DC branch. However, this method significantly increases power losses, which are undesirable. The second method aims at changing the voltage difference between two DC terminals to regulate the branch current. This method is considered a preferable approach since the branch current control is achieved with acceptable low power losses.

C. Equivalent Power Flow Analysis of the Meshed MTDC Grid

The DC nominal voltage of T 3 is regulated at ± 50 kV, via the control of MMC3. Hence, the DC voltage of T3, V_3 , is 100 kV. T1 imports 1 kA to the DC grid, while T2 exports 0.4 kA from the DC grid. Thus, the DC currents I_1 and I_2 are 1 kA and 0.4 kA, respectively. The DC buses of MMC1 and MMC2 can be simplified and regarded as two ideal DC current sources, while the DC bus of MMC3 can be

considered as an ideal voltage source. The diagram in Fig. 2 presents the simplifications of the DC grid. An equivalent power flow analysis is conducted based on Fig. 2

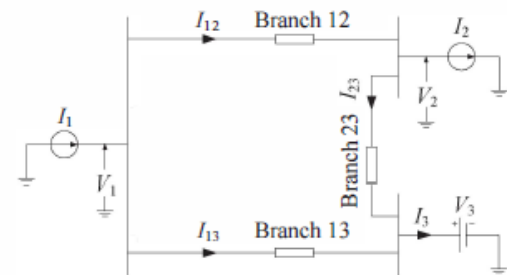


Fig. 2. Configuration of the 3-T MMC-HVDC grid.

Based on Kirchhoff Current Law (KCL), the currents in the meshed MTDC grid have the following relationships:

$$I_1 = I_{12} + I_{13} \tag{1}$$

$$I_3 = I_{13} + I_{23} \tag{2}$$

$$I_1 = I_2 + I_3. \tag{3}$$

According to Ohm's Law, the branch currents in Fig. 2 can be derived as:

$$I_{12} = \frac{V_1 - V_2}{R_{12}} \tag{4}$$

$$I_{13} = \frac{V_1 - V_3}{R_{13}} \tag{5}$$

$$I_{23} = \frac{V_2 - V_3}{R_{23}}. \tag{6}$$

The values of I_1 , I_2 , and V_3 are determined by the control of MMCs, which means there are six unknown variables in (1)-(6). Therefore, all of their values can be obtained. The expressions of V_1 , V_2 and I_3 are derived in (7)-(9).

$$V_1 = V_3 + \frac{I_1 R_{13}(R_{12} + R_{23}) - I_2 R_{23} R_{13}}{R_1 + R_2 + R_3} \tag{7}$$

$$V_2 = V_3 + \frac{I_1 R_{23} R_{13} - I_2 R_{23}(R_{12} + R_{13})}{R_1 + R_2 + R_3} \tag{8}$$

$$I_3 = I_1 - I_2. \tag{9}$$

Referring to the cable parameters listed in Table I and the output DC currents and voltage of the MMCs where $I_1 = 1$ kA, $I_2 = 0.4$ kA, $V_3 = 100$ kV, the

voltages and currents of the DC grid are: DC terminal voltages: $V_1 = 100.8$ kV, $V_2 = 100.2$ kV, $V_3 = 100$ kV; DC terminal currents: $h = 1$ kA, $h = 0.4$ kA, $h = 0.6$ kA; DC branch currents: $h_2 = 0.6$ kA, $h_3 = 0.4$ kA, $123 = 0.2$ kA. From the power flow analysis, it is observed that the branch current h_2 is larger than 113. However, the cables in both branches are made from the same material and thus have the same transfer capability. Hence, Branch 12 is either overloaded or getting closer to its transfer limit, while Branch 13 is insufficiently utilized. Under this condition, a DC CFC with the capability of balancing the branch currents is needed for the meshed MTDC grid to deal with the aforementioned problem, which is discussed in the following sections. The structure of the DC CFC is presented in Section III, the control design is discussed in Section IV and its dynamic performance on balancing and regulating the branch currents is shown in Section V.

III. DC CURRENT FLOW CONTROLLER

A. Meshed MTDC System with a DC CFC

The DC CFC is equipped on Branch 12 and Branch 13. Fig. 3 shows a single-line schematic diagram of the developed meshed 3-T MMC-HVDC system with the DC CFC being equipped

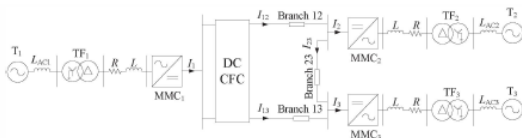


Fig. 3. 3-T MMC-HVDC system with the installation of the DC CFC.

The expected operating condition of the power transmission system is to keep each branch working at its optimal transfer capacity, and this is achievable by the control of the DC CFC, which will be explained in Section IV.

B. Structure of the DC CFC

The detailed structure of the DC CFC installed between Branch 12 and Branch 13 is shown in Fig. 4.

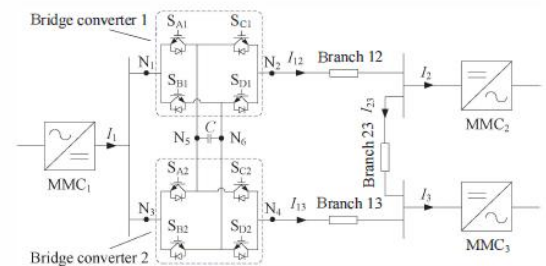


Fig. 4. Structure of the DC CFC.

The DC CFC is composed of two identical full-bridge DC-DC converters. Fig. 5 shows the diagram of an independent full bridge DC-DC converter. A DC-DC converter has two legs and one energy storage capacitor in the middle. Each leg comprises two IGBTs with their anti-parallel diodes. SA and SB are on the same leg,

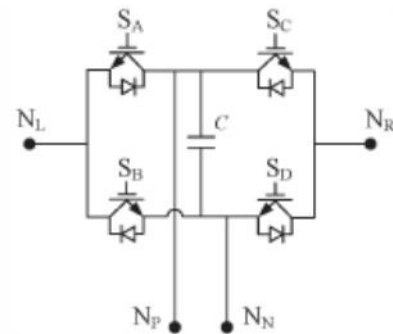


Fig. 5. Full bridge DC-DC converter.

while the other two (Sc and So) compose the other leg. Two switches on the same leg are switched complementary. Consequently, when SA is in its off state, SB is in the on state, which is the same for Sc and So. This operation manner ensures the two switches on the same leg are never off simultaneously. Under this switching specification, the current through the capacitor is always continuous. The capacitor has three operational states with different switching modes, bypassed, charged, and discharged, as shown in Table II. The capacitor is bypassed when both SA and Sc are on or both SB and So are on. The capacitor is charged when both SA and So are on, while the capacitor is discharged, when both SB and Sc are on. The DC CFC is composed of the connection of two DC-DC converters.

The interconnection points are the positive and negative side (N p, N N) of the capacitor, respectively, as shown in Fig. 5.

TABLE II
SWITCHING MODES WITH CAPACITOR STATUS

Mode	Capacitor State	S _A	S _B	S _C	S _D
1	Bypassed	ON	OFF	ON	OFF
2	Bypassed	OFF	ON	OFF	ON
3	Charged	ON	OFF	OFF	ON
4	Discharged	OFF	ON	ON	OFF

IV. CONTROL STRATEGY OF THE DC CFC

The objective of using the DC CFC is to realize the control of the branch currents in the meshed 3-T MMC-HVDC grid. The equivalent power flow analysis of the meshed MTDC grid derived in Section II-C indicates that it is necessary to balance the current distribution of Branch 12 and Branch 13 through the control of the DC CFC. This is achieved by transferring additional power from Branch 12 to Branch 13 via the energy storage capacitor. Under steady-state condition, the imported and exported power of the interconnected capacitor should be equal. In order to realize the control of the branch currents, not only the branch current h2 needs to be regulated, but the voltage of the interconnected capacitor Vc needs to be controlled as well. This is because a stable capacitor voltage indicates that the power exchange of the capacitor is balanced. In addition, the voltage of a capacitor represents the energy stored in the capacitor. The energy of a capacitor can be approximately represented by its average voltage: Vc is the average voltage stored in the capacitor. The power imported and exported of a capacitor is depicted in Fig. 6.

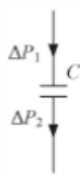


Fig. 6. Power imported and exported of a capacitor

Under steady-state conditions, the power exchange of the interconnected capacitor is balanced, so

$$\Delta P_1 = \Delta P_2 \tag{11}$$

That means the power imported to the capacitor equals to the power exported from the capacitor, so the power of the capacitor Pc is zero. As power is defined as the derivative of work, we have

$$\frac{dW_c}{dt} = P_c = 0. \tag{12}$$

According to (10) and (12), the capacitor voltage should be controlled to maintain a certain value under steady-state conditions based on the operating condition of the DC CFC. The basic control strategy of the DC CFC is similar to that of a point-to-point VSC-HVDC system in which DC voltage control is employed by one converter station at one terminal to maintain the voltage of the DC grid, while active power flow control is applied by the other converter station to determine the amount of power and its direction. Therefore, one terminal is operated as a slack DC terminal, the active power of which can be either imported or exported depending on the operating condition of the other terminal.

For the DC CFC modeled in this paper, the DC-DC converter on Branch 12 is assigned to control branch current h2. When h2 is controlled, the current on Branch 13, h3, is determined simultaneously. This is because the total current of Branch 12 and Branch 13, h, is regulated at 1 kA by the control of MMCI. The other DC-DC converter on Branch 13 is responsible for regulating the voltage of capacitor C2. Since two capacitors of the DC-DC converters are connected in parallel, they always have the same voltage value.

From the structure of the DC CFC, as shown in Fig. 4, SA1 and SA2 are connected in parallel, and this is same for SB1 and SS2. Thus, to maintain the voltage of the capacitors, the operation of SA1 and SA2 must be synchronous. Meanwhile, the operation of SB1 and SS2 are also synchronous and should be complementary with SA1 and SA2. In fact, in the existence of SA1 and SSI of the DC-DC converter on Branch 12, SA2 and SS2 of the other DC-DC converter are not necessarily needed.

However, in order to maintain a higher redundancy of the meshed DC grid, four switches on both DC-DC converters are retained. In addition, SA1 and SA2 are both named as SA with a same controlled gating signal (GA) while SSI and SS2 are both named as Ss with a complementary gating signal (Gs). discharged with mode 4. The duty ratio of each switch of the full-bridge DC-DC converter is DSJ (J = A, B, C, D) and the generated gating signal is Gs./.. The gating signal is produced through the PWM by comparing the controlled signal with a sawtooth wave. Two general operating states within two switching cycles are depicted in Fig. 7.

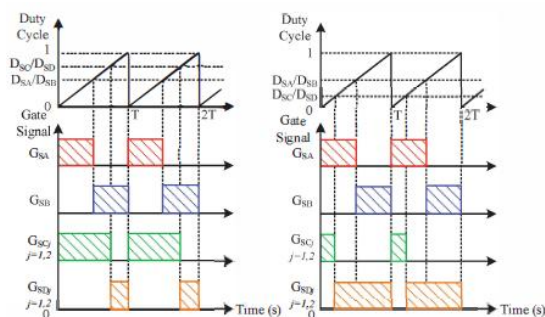


Fig. 7, Gate signal generation of each switch.

According to Table II, the capacitor of the DC-DC converter shown in Fig. 5 is charged with switching mode 3 and isIn Fig. 4, Branch 12 needs to transfer its power to Branch 13 by charging the interconnected capacitor, while Branch 13 needs to obtain the excess power from Branch 12 by discharging the interconnected capacitor. Due to the characteristics of complementary switching, only one switch on one leg needs to be controlled. For the DC CFC modeled in this paper, SA, SCI and SC2 are controlled. In order to simplify the following analysis, the duty ratios of SA and Ss, DSA, and Dss, are both fixed at 0.5. The branch current control and the capacitor voltage control will thus be achieved by controlling the switches SCI and SC2.

A. Branch Current Control:

The branch current control is implemented through regulating the difference of the duty ratio between SA and SCI.

As the switching performance is shown in Fig. 7, when the duty ratio difference DSA -DSCI is positive, the interconnected capacitor is charged and h2 becomes smaller. The measurement of branch current I2 (I2meas) is tracking its reference value (I2ref) by the application of a PI controller. Fig. 8 illustrates the control approach.

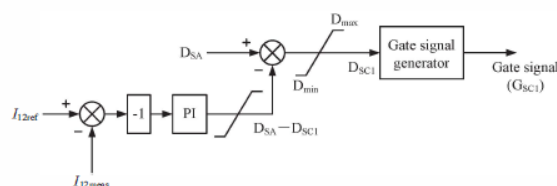


Fig. 8, Branch current control system.

In Fig. 8, a larger (smaller) h2meas than h2ref leads to a decrease (increase) in the generated duty cycle of SCI. Thus, the interconnected capacitor will be charged (discharged) more with switching mode 3 and h2meas will be reduced (increased) to track h2ref.

B. Capacitor Voltage Control:

The control of the capacitor voltage is similar to that of the branch current, via a PI controller. The control approach is illustrated in Fig. 9. A positive (negative) tracking error signifies the measured capacitor voltage is smaller (larger) than its reference voltage and will result in a decrease (increase) in the generated duty ratio of SC2. Thus, the interconnected capacitor will be charged (discharged) with switching mode 3 and Vcmeas will be increased (decreased) to track the control reference

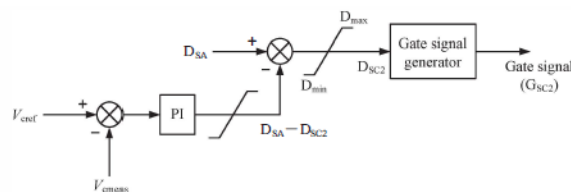


Fig. 9. Capacitor voltage control.

C. Start-up Process of the DC CFC

Initially, the DC CFC is in standby mode, i.e., the interconnected capacitor is bypassed, so the capacitor voltage is zero and the branch currents are not controlled. I1meas (0.6 kA) is larger than the reference

value (0.5 kA). When the DC CFC is started to control the branch currents, the duty ratios of SCI and SC2 generated are both decreased, leading to the charging of the interconnected capacitor. Hence, the voltage of the interconnected capacitor will be fast charged to the reference value and I_{meas} will decrease during the charging process. A new steady-state condition will be achieved when the power exchanged between the capacitor is balanced, the voltage of the capacitor is well maintained and the branch current is regulated to the reference value.

D. Features of the DC CFC

The DC CFC with the proposed control has two main features: 1) branch current balancing capability; 2) branch current regulating capability.

1) Branch Current Balancing Capability: In the meshed MTDC grid, an equal distribution of the branch currents at one terminal junction is expected when the transmission capabilities of both cables are the same. However, different lengths of branches and the impact of the environmental conditions lead to different cable resistance, so the natural distribution of branch currents in a meshed grid is usually unbalanced. The DC CFC with the proposed control in the meshed MTDC grid has the ability to regulate the DC branch current to achieve equal distribution of the branch currents.

2) Branch Current Regulating Capability: In some meshed MTDC applications, the transmission capabilities of DC cables are different; it is expected that the power transfer on one branch can be operated over another branch. Hence, instead of the equal DC branch current distribution, a proportional distribution of the branch currents at one terminal junction is desired to facilitate the optimal operating state of each branch. The DC CFC with the proposed control is capable of regulating the DC branch currents within a certain range. The balancing and regulating capabilities of the branch current of the DC CFC will be verified through case studies in the next section.

V. SIMULATION SYSTEMS AND CASE STUDIES

A. Simulation Systems

The simulation system is the meshed 3-T MMC-HVDC system, as shown in Fig. 3, using the parameters provided in Table I. The structure of the DC CFC used in the simulation is the same as shown in Fig. 4. The interconnect capacitor C is 30 fLF, and the rated voltage of the IGBTs of the DC CFC is 10 kV. The control strategies of the MMCs and the control strategy of the DC CFC are explained in Section II-B and Section IV, respectively.

B. Case Studies

1) Branch Current Balancing Capability: Initially, the DC CFC is in standby mode. The DC CFC starts to balance the branch currents at 1 s. h_{2ref} is set to 0.5 kA, and V_{ref} is set to 1 kV. Fig. 10(a) shows the MMC DC side current at each terminal. $h_1 = 1$ kA, $h_2 = 0.4$ kA, and $h_3 = 0.6$ kA. Only a small ripple is observed at 1 s when the DC CFC is operating to balance the branch currents. Fig. 10(b) shows three branch currents with natural distributions of each terminal before 1 s. It is observed that by bypassing the interconnected capacitor at the initial stage, the DC CFC does not perform the balancing function of the branch currents and the system is operating just as the system shown in Fig. 1(a) without the DC CFC in which branch current $h_2 = 0.6$ kA, $h_3 = 0.4$ kA, and $h_1 = 0.2$ kA. At 1 s, both h_2 and h_3 reach 0.5 kA within 0.5 s. The DC voltage of the interconnected capacitor is shown in Fig. 10(c). The capacitor voltage V_c is zero when the capacitor is bypassed. At 1 s, the power is transferred via the capacitor, and V_c is charged to 1 kV within 0.1 s. The DC side voltage of MMC3 is shown in Fig. 10(d). V_3 has a small voltage drop when the balancing control of the branch current is applied, but it resumes 100 kV within 1 s. The overall results indicate the effectiveness of the DC CFC in balancing the branch currents.

2) Branch Current Regulating Capability: Initially, the DC CFC is with the mode of balancing the branch current. At 1 s, the logic of regulating the branch currents performs its role where h_{2ref} is set to 0.7 kA,

and V_{ref} is still set to 1 kV Fig. 10(a) demonstrates that the currents at three DC terminals are well stabilized at their reference values after applying the branch current regulating control of the DC CFC. Fig. 11 (b) shows that at 1 s, h_2 is tracking its reference signal to 0.7 kA, while h_3 is decreasing to 0.3 kA and 123 is increasing to 0.3 kA; both branch currents reach the new steady-state condition within 1 s. The voltage of the interconnected capacitor is maintained at 1 kV with a small voltage dip as shown in Fig. 10(c). The DC voltage of T3 and V3, has a slight decrease after 1 s, but it returns to the reference voltage within 1 s, as shown in Fig. 10(d). The performance of the branch currents and capacitor voltage validate the branch current regulating control of the DC CFC. The simulation results shown in Fig. 10 and Fig. 11 illustrate that the branch currents can not only be equally distributed, but also be regulated within a certain range as well.

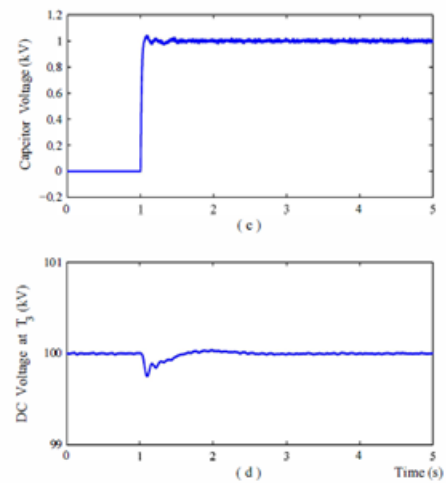
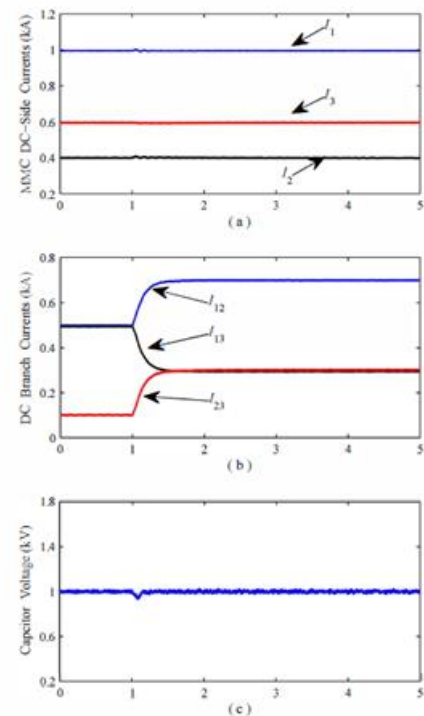
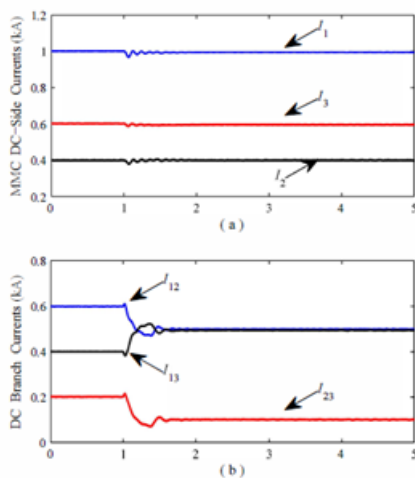


Fig. 10. System performance with branch current balancing control of the DC CFC. (a) i_1, i_2, i_3 are the DC currents of T1, T2, and T3, respectively. (b) h_2, h_3, h_3 are the DC currents of Branch 12, 13, and 23, respectively (c) Voltage of the interconnected capacitor (d) DC voltage at T3.



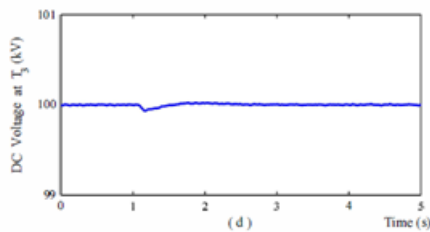


Fig. 11. System performance with branch current regulating control of the DC CFC. (a) h_1, h_2, h_3 are the DC currents of T 1, T 2, and T 3, respectively. (b) $h_{12}, h_{13},$ and h_{23} are the DC currents of Branch 12, 13, and 23, respectively (c) Voltage of the interconnected capacitor (d) DC voltage at T3.

VI. CONCLUSION:

This paper has proposed the design of a DC CFC, particularly its detailed control strategy, in order to control the branch currents in a meshed 3-T MMC-HVDC grid. An equivalent power flow analysis of the 3-T meshed grid under the steady-state condition has been derived. It has been found that for the system without DC CFC, the branch currents are uncontrollable and one or more branches may become either overloaded or underutilized. The structure of the DC CFC has been presented with its operation theorem being proposed. The DC CFC has features of branch current balancing and regulating capability with the proposed control, which has been validated through case studies on the RTDS. These features allow to operate under optimal condition. This DC CFC with the proposed control can be regarded as an effective model for the branch current control in a meshed DC grid.

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