

A Grid-Connected Dual Voltage Source Inverter with Power Quality Improvement Features

**Swetha Miryala**

**M.Tech-Power Electronics,
Department of EEE,
SRTIST Nalgonda, Telangana.**

**S. Sreenu**

**Associate Professor,
Department of EEE,
SRTIST Nalgonda, Telangana.**

ABSTRACT

This paper presents a dual voltage source inverter (DVSI) scheme to enhance the power quality and reliability of the micro grid system. The proposed scheme is comprised of two inverters, which enables the micro grid to exchange power generated by the distributed energy resources (DERs) and also to compensate the local unbalanced and nonlinear load. The control algorithms are developed based on instantaneous symmetrical component theory (ISCT) to operate DVSI in grid sharing and grid injecting modes. The proposed scheme has increased reliability, lower bandwidth requirement of the main inverter, lower cost due to reduction in filter size, and better utilization of micro grid power while using reduced dc-link voltage rating for the main inverter. These features make the DVSI scheme a promising option for micro grid supplying sensitive loads. The topology and control algorithm are validated through extensive simulation and experimental results.

INTRODUCTION

Technological progress and environmental concerns drive the power system to a paradigm shift with more renewable energy sources integrated to the network by means of distributed generation (DG). These DG units with coordinated control of local generation and storage facilities form a micro grid. In a micro grid, power from different renewable energy sources such as fuel cells, photovoltaic (PV) systems, and wind energy systems are interfaced to grid and loads using power

electronic converters. A grid interactive inverter plays an important role in exchanging power from the micro grid to the grid and the connected load. This micro grid inverter can either work in a grid sharing mode while supplying a part of local load or in grid injecting mode, by injecting power to the main grid.

Maintaining power quality is another important aspect which has to be addressed while the micro grid system is connected to the main grid. The proliferation of power electronics devices and electrical loads with unbalanced nonlinear currents has degraded the power quality in the power distribution network. Moreover, if there is a considerable amount of feeder impedance in the distribution systems, the propagation of these harmonic currents distorts the voltage at the point of common coupling (PCC). At the same instant, industry automation has reached to a very high level of sophistication, where plants like automobile manufacturing units, chemical factories, and semiconductor industries require clean power. For these applications, it is essential to compensate nonlinear and unbalanced load currents.

Load compensation and power injection using grid interactive inverters in micro grid have been presented in the literature. A single inverter system with power quality enhancement is discussed in. The main focus of this work is to realize dual functionalities in an inverter that would provide the active power injection from a solar PV system and also works as an active power

filter, compensating unbalances and the reactive power required by other loads connected to the system.

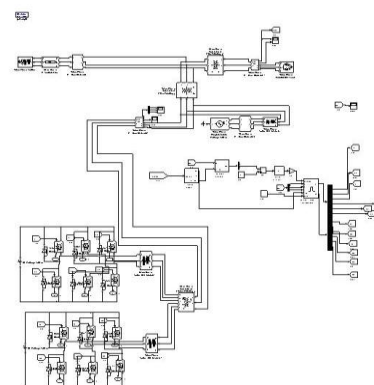
A voltage regulation and power flow control scheme for a wind energy system (WES) is proposed. A distribution static compensator (DSTATCOM) is utilized for voltage regulation and also for active power injection. The control scheme maintains the power balance at the grid terminal during the wind variations using sliding mode control.

A multifunctional power electronic converter for the DG power system is described in. This scheme has the capability to inject power generated by WES and also to perform as a harmonic compensator. Most of the reported literature in this area discuss the topologies and control algorithms to provide load compensation capability in the same inverter in addition to their active power injection. When a grid-connected inverter is used for active power injection as well as for load compensation, the inverter capacity that can be utilized for achieving the second objective is decided by the available instantaneous micro grid real power. Considering the case of a grid-connected PV inverter, the available capacity of the inverter to supply the reactive power becomes less during the maximum solar isolation periods. At the same instant, the reactive power to regulate the PCC voltage is very much needed during this period. It indicates that providing multi functionalities in a single inverter degrades either the real power injection or the load compensation capabilities.

This paper demonstrates a dual voltage source inverter (DVSI) scheme, in which the power generated by the micro grid is injected as real power by the main voltage source inverter (MVSI) and the reactive, harmonic, and unbalanced load compensation is performed by auxiliary voltage source inverter (AVSI). This has an advantage that the rated capacity of MVSI can always be used to inject real power to the grid, if sufficient renewable power is available at the dc link. In the DVSI scheme, as total load power is supplied by two inverters, power losses across the semiconductor

switches of each inverter are reduced. This increases its reliability as compared to a single inverter with multifunctional capabilities. Also, smaller size modular inverters can operate at high switching frequencies with a reduced size of interfacing inductor, the filter cost gets reduced. Moreover, as the main inverter is supplying real power, the inverter has to track the fundamental positive sequence of current. This reduces the bandwidth requirement of the main inverter. The inverters in the proposed scheme use two separate dc links. Since the auxiliary inverter is supplying zero sequence of load current, a three-phase three-leg inverter topology with a single dc storage capacitor can be used for the main inverter. This in turn reduces the dc-link voltage requirement of the main inverter. Thus, the use of two separate inverters in the proposed DVSI scheme provides increased reliability, better utilization of micro grid power, reduced dc grid voltage rating, less bandwidth requirement of the main inverter, and reduced filter size. Control algorithms are developed by instantaneous symmetrical component theory (ISCT) to operate DVSI in grid-connected mode, while considering non stiff grid voltage. The extraction of fundamental positive sequence of PCC voltage is done by dq0 transformation. The control strategy is tested with two parallel inverters connected to a three-phase four-wire distribution system. Effectiveness of the proposed control algorithm is validated through detailed simulation and experimental results.

CIRCUIT DIAGRAM



POWER QUALITY

The contemporary container crane industry, like many other industry segments, is often enamored by the bells and whistles, colorful diagnostic displays, high speed performance, and levels of automation that can be achieved. Although these features and their indirectly related computer based enhancements are key issues to an efficient terminal operation, we must not forget the foundation upon which we are building. Power quality is the mortar which bonds the foundation blocks. Power quality also affects terminal operating economics, crane reliability, our environment, and initial investment in power distribution systems to support new crane installations.

To quote the utility company newsletter which accompanied the last monthly issue of my home utility billing: 'Using electricity wisely is a good environmental and business practice which saves you money, reduces emissions from generating plants, and conserves our natural resources.' As we are all aware, container crane performance requirements continue to increase at an astounding rate. Next generation container cranes, already in the bidding process, will require average power demands of 1500 to 2000 kW – almost double the total average demand three years ago. The rapid increase in power demand levels, an increase in container crane population, SCR converter crane drive retrofits and the large AC and DC drives needed to power and control these cranes will increase awareness of the power quality issue in the very near future.

CONTROLLING SYSTEM

The concept of instantaneous reactive power is used for the controlling system. Following this, the 3-phase voltage upon the use of the park presented by Akagi [24] has been transformed to the synchronous reference frame (Park or dq0 transformation). This transformation leads to the appearances of three instantaneous space vectors: V_d on the d-axis (real or direct axis), V_q on the q-axis (imaginary or quadrature axis) and V_0 , from the 3-phase voltage of V_a , V_b and

V_c . The related equations of this transformation, expressed in the MATLAB software, are as follows:

$$V_d = \frac{2}{3} (V_a \sin(\omega t) + V_b \sin(\omega t - \frac{2\pi}{3}) + V_c \sin(\omega t + \frac{2\pi}{3})) \quad (2)$$

$$V_q = \frac{2}{3} (V_a \cos(\omega t) + \cos(\omega t - \frac{2\pi}{3}) + \cos(\omega t + \frac{2\pi}{3})) \quad (3)$$

$$V_0 = \frac{1}{3} (V_a + V_b + V_c) \quad (4)$$

A dynamic computation shows that the voltage oscillations in the connecting node of the flicker-generating load to the network are created by 3 vectors: real current (i_p), imaginary current (i_q) and the derivative of the real current with respect to time ($\frac{di_p}{dt}$). In general, for the complete voltage flicker compensation, the compensating current (i_c) regarding the currents converted to the dq0 axis is given as [3]:

$$i_c = j(i_q + i_p \frac{R}{X} f + \frac{1}{\omega} \frac{di_p}{d\omega} f + k)$$

where R and X are the synchronous resistance and reactance of the line and f is the correcting coefficient. The constant k is also used to eliminate the average reactive power of the network [3]. If the compensation current of the above equation is injected to the network, the whole voltage flicker existing in the network will be eliminated. Regarding the equation, related to the dq-transformation of the 3-phase-voltages to the instantaneous vectors, it is obvious that under the conditions of accessing an average voltage flicker, V_d and V_0 , the obtained values are close to zero and V_q is a proper value adapting to the voltage oscillation of the network.

This state of the 3-phase voltage flicker is presented in the following figures (simulated in the MATLAB Simulink package):

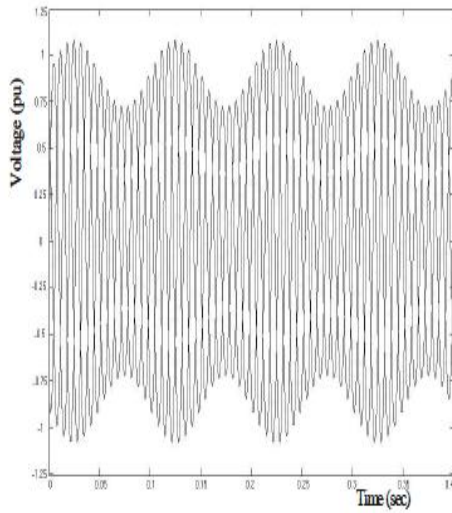


Figure 1: The voltage flicker exerted to the circuit

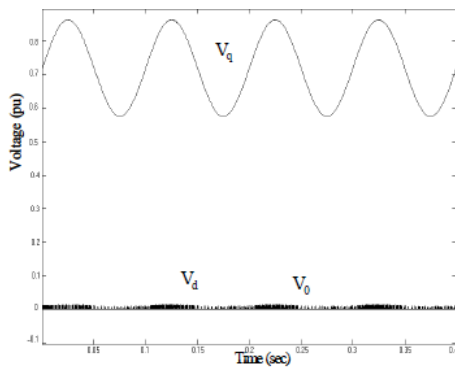
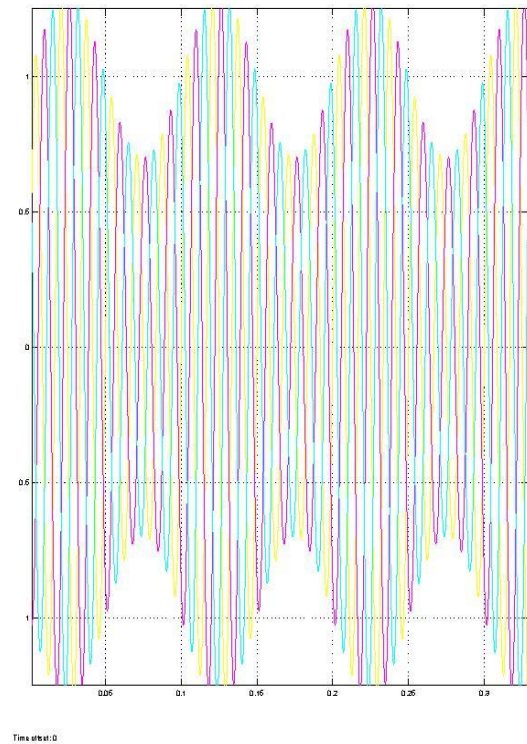
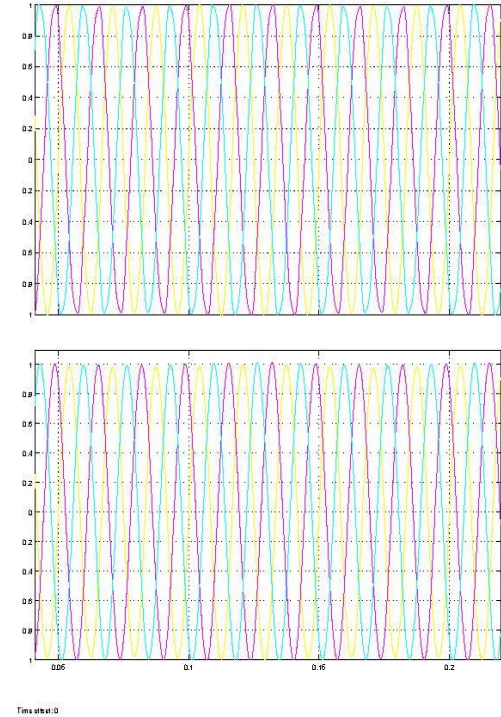
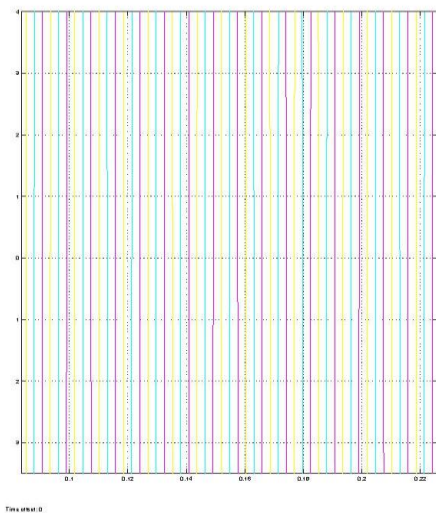
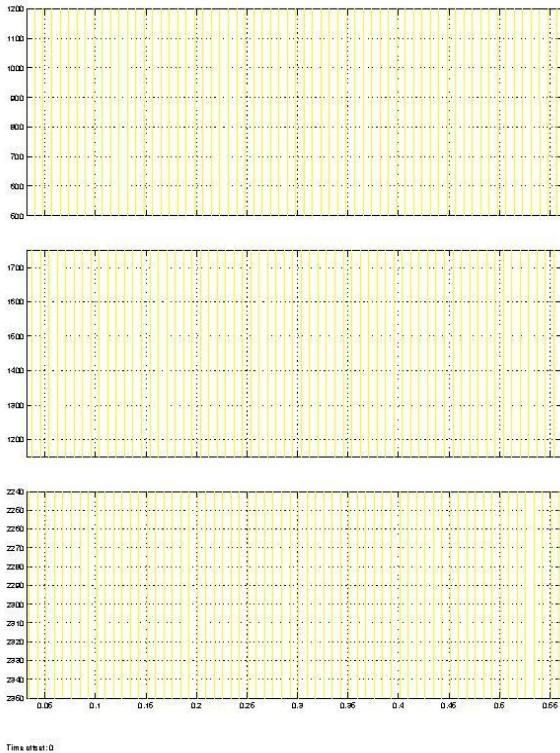


Figure 2: The instantaneous components of the 3-phase voltage flicker

SIMULATION RESULTS





CONCLUSION

A DVSI scheme is proposed for micro grid systems with enhanced power quality. Control algorithms are developed to generate reference currents for DVSI using ISCT. The proposed scheme has the capability to exchange power from distributed generators (DGs) and also to compensate the local unbalanced and non linear load. The performance of the proposed scheme has been validated through simulation and experimental studies.

As compared to a single inverter with multi functional capabilities, a DVSI has many advantages such as, increased reliability, lower cost due to the reduction in filter size, and more utilization of inverter capacity to inject real power from DGs to micro grid. Moreover, the use of three-phase, three wire topology for the main inverter reduces the DC link voltage requirement. Thus, a DVSI scheme is a suitable interfacing option for micro grid supplying sensitive loads.

REFERENCES

- [1] J. Sun, D. Czarkowski, Z. Zabar, "Voltage Flicker Mitigation Using PWM-Based Distribution STATCOM", IEEE Power Engineering Society Summer Meeting, Vol.1, (21-25 July 2002), pp. 616-621.
- [2] J. Mckim, "The UIE Flicker-meter Demystified", Hewlett- Packard's Power Products Division, 1997.
- [3] R. Collantes-Bellido, T. Gomez, "Identification and Modeling of a Three Phase Arc Furnace for Voltage Distribution Simulation", IEEE Trans. on Power Delivery; Vol.12, No.4, (1997), pp. 1812-1817.
- [4] L. Tang, S. Kolluri, M.F. McGranaghan, "Voltage Flicker Prediction for Two Simultaneously Operated AC Arc Furnaces" IEEE Trans. on Power Delivery; Vol.12, No.2, (1997), pp. 985-991.
- [5] M. Zouiti, S. Saadate, X. Lombard, C. Poumarede, C. Levillain, "Electronic Based Equipment for Flicker Mitigation", Proceedings of International Conference on Harmonics And Quality of Power, Vol.2, (1998), pp. 1182-1187.
- [6] T. Larsson, C. Poumarede, "STATCOM, an efficient means for flicker mitigation" IEEE Power Engineering Society Winter Meeting, Vol.2, (Jan-4Feb 1999), pp. 1208-1213.
- [7] C. S. Chen, H. J. Chuang, C. T. Hsu, S. M. Tscng, "Stochastic Voltage Flicker Analysis and Its Mitigation for Steel Industrial Power Systems", IEEE Power Tech Proceedings, Vol.1, (10-13 Sept. 2001).
- [8] Z. Zhang, N. R. Fahmi, W. T. Norris, "Flicker Analysis and Methods for Electric Arc Furnace Flicker (EAF) Mitigation (A Survey)", IEEE Power Tech Proceedings, Vol.1, (10-13 Sept. 2001).
- [9] J. R. Clouston, J. H. Gurney, "Field Demonstration of a Distribution Static Compensator Used to Mitigate Voltage Flicker", IEEE Power Engineering Society

Winter Meeting, Vol.2, (31 Jan-4 Feb 1999), pp. 1138-1141.

[10] A. Elnady, W. El-khattam, M. A. Salama, "Mitigation of AC Arc Furnace Voltage Flicker Using the Unified Power Quality Conditioner", IEEE Power Engineering Society Winter Meeting, Vol.2, (27-31 Jan. 2002), pp. 735-739.

[11] S. Suzuki, Y. Hara, E. Masada, M. Miyatake, K. Shutoh, "Application of Unified Flow Controller for Power Quality Control at Demand Side", The Third International Power Electronics and Motion Control Conference Proceedings (PIEMC 2000), Vol.3 (15-18Aug 2000), pp. 1031-1036.

[12] Y. Hara, E. Masada, M. Miyatake, K. Shutoh, "Application of Unified Flow Controller for Improvement of Power Quality" IEEE Power Engineering Society Winter Meeting, Vol.4, (23-27 Jan. 2000), pp. 2600-2606.

[13] J. H. R. Enslin, "Unified Approach to Power Quality Mitigation" International Symposium on Industrial Electronics (ISIE '98), IEEE Proceedings, Vol.1, (July1998), pp. 8-20.

[14] B. P. Roberts, "Power Quality Solution Alternatives for Low and Medium Voltage Continuous Process Loads", IEEE Rural Electric Power Conference, (5-7 May 2002), pp. C4-C4_7.

[15] G. C. Montanari, M. Loggini, L. Pitti, E. Tironi, D. Zaninelli, "The effects of series inductors for flicker reduction in electric power systems supplying arc furnaces", IEEE Industry Applications Society Annual Meeting, Vol.2, (2-8 Oct. 1993), pp.1496-1503.

[16] M. W. Marshall, "sing Series Capacitors to Mitigate Voltage Flicker Problems" IEEE Rural Electric Power Conference, (20-22 April 1997), pp. B3-1-5.

[17] J. Dolezal, A. G. Castillo, V. Valouch, "Topologies and control of active filters for flicker compensation", International Symposium on Industrial Electronics, IEEE Proceedings, Vol.1, (4-8 Dec, 2000), pp. 90-95.

[18] L. Gyugi, A. A. Otto, "Static Shunt Compensation for Voltage Flicker Reduction and Power Factor Correction", American Power Conference (1976), pp. 1272-1286.

Author Details:

I hereby declare that the project work entitled "**A Grid-Connected Dual Voltage Source Inverter with Power Quality Improvement Features**" submitted to the JNTU Hyderabad, is a record of an original work done by me under the guidance of **Mr. S.Sreenu**, Department of EEE, **SWAMI RAMANANDA THIRTHA INSTITUTE OF SCIENCE & TECHNOLOGY**, and this project work is submitted in the partial fulfillment of the Requirements for the award of the degree of Master of Technology in EEE. The results embodied in this thesis have not been submitted to any other University or Institute for the award of any degree.